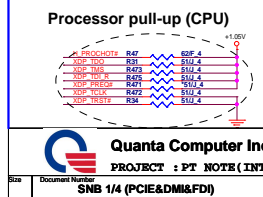
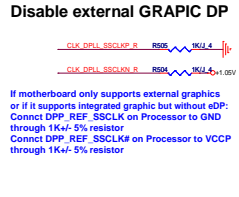
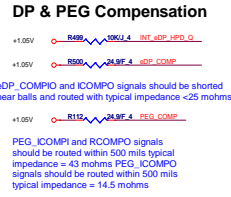
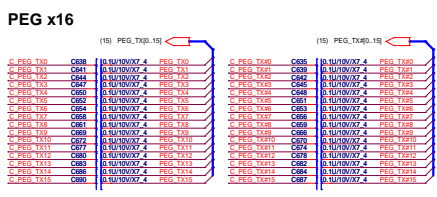
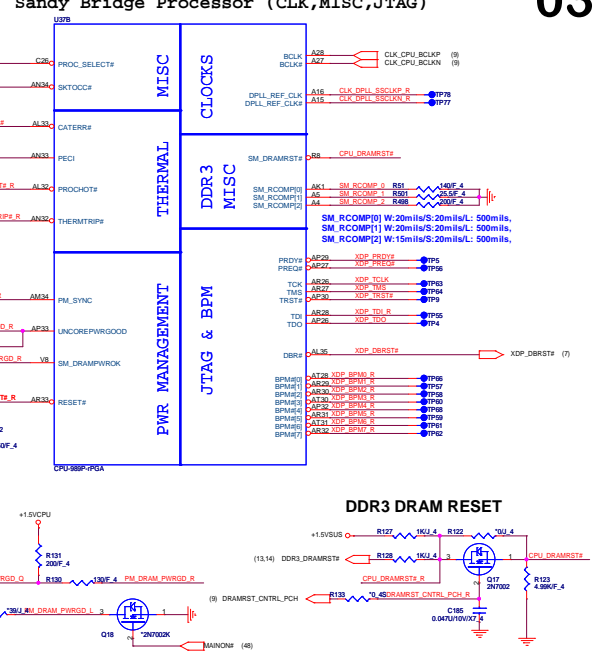
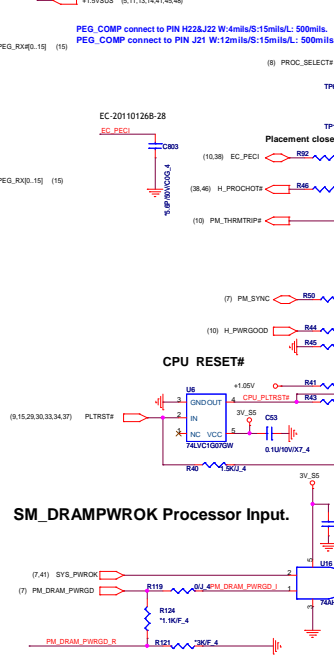


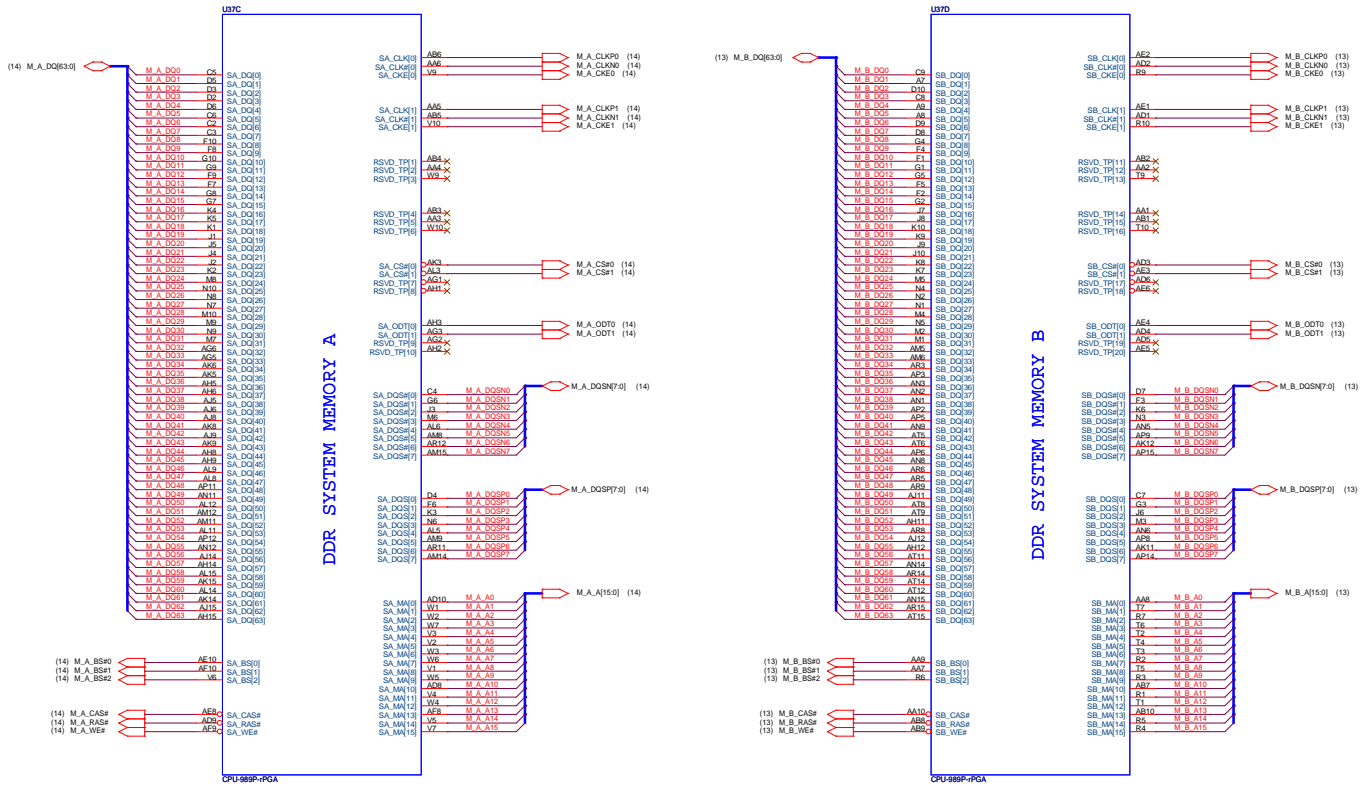
Table of Contents

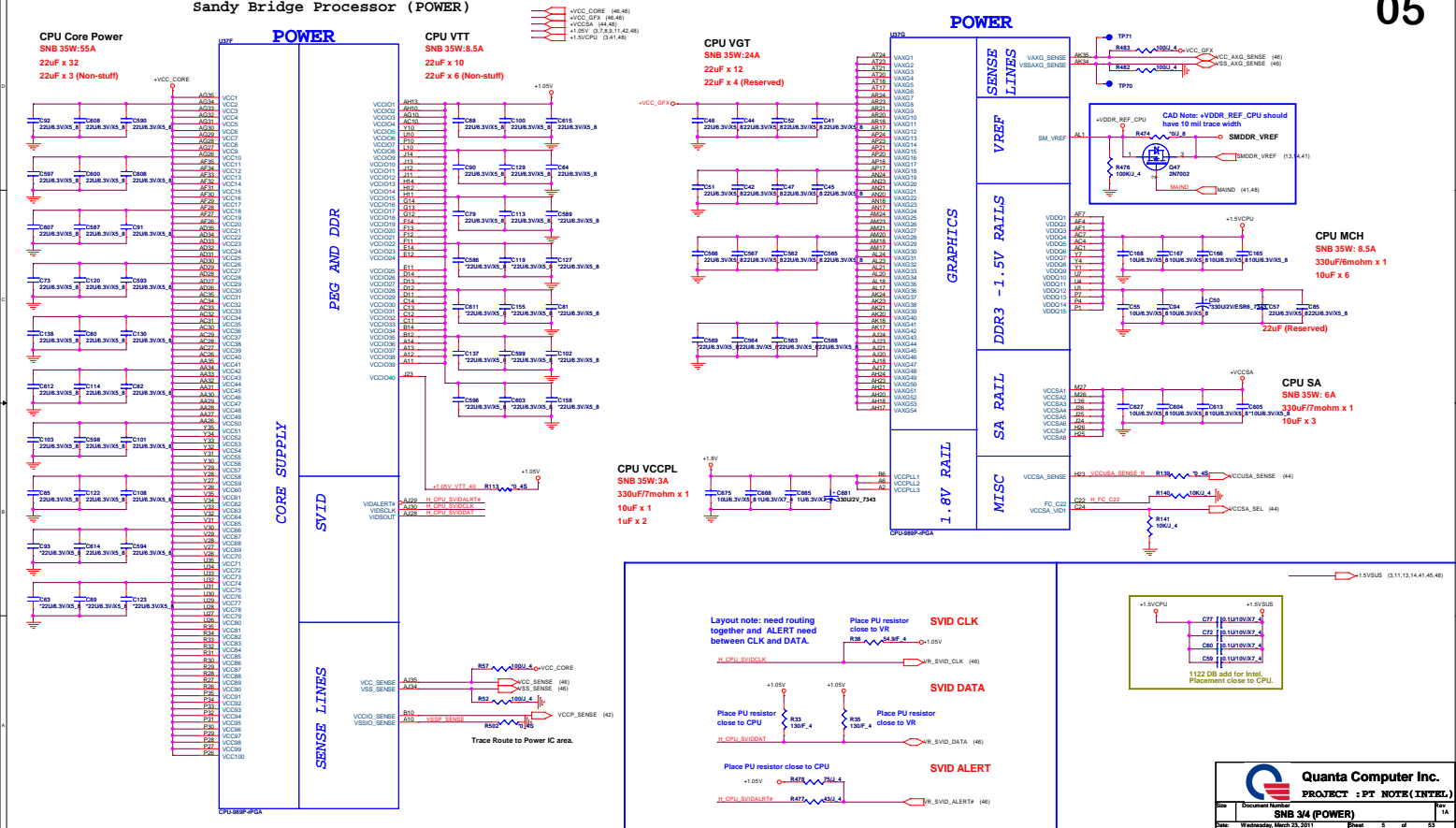
PAGE	DESCRIPTION
01	BLOCK DIAGRAM
02	FRONT PAGE
03-06	SNB
07-12	PCH
13-14	DDRIII SO-DIMM
15-19	Madison
20-21	VRAM
22	KB, TP, TrackPoint
23	LCD + CAMERA CONN
24	HDMI
25	CRT
26	HDD
27	USB X1/USB+ESATA
28	CONN USB X2
29	MINI-Card (WLAN/Wimax)
30	MINI-Card (SSD, WWAN)
31	BLUETOOTH
32	AUDIO CODEC (CX20671-21Z)
33	LAN: AR8151-BL1A-R
34	Card Reader(RTS5209)
35	FAN & THERMAL
36	G-SENSOR/LID SWITCH/LED
37	RFID EEPROM
38	KBC IT8518/19
39	HOLE & SCREW
40	SYSTEM 5V/3V (RT8206M)
41	+1.5VSUS (UP6163BQAG)
42	+1.05V (OZ8117)
43	+1.8V (HPA00835RTER)
44	+VCCSA (OZ8117)
45	+VGPU_CORE (OZ8117)
46	+VCC_CORE(ISL95831)
47	Charger (ISL88731A)
48	Discharge
49	Power Block Diagram
50	Schematic Value Descript

Power States

POWER PLANE	VOLTAGE	PAGE	DESCRIPTION	CONTROL SIGNAL	ACTIVE IN
VIN	10V~+20V	23,40,41,42,44,45,46,,47,48	MAIN POWER		S0~S5
+3V_RTC	+3.0V~+3.3V	7,8,11,38	RTC		S0~S5
3VPCU	+3.3V	7,8,22,23,31,33,36,38,40,43,45,47,48	ITE8518 POWER	3V5V_EN	S0~S5
5VPCU	+5V	8,38,40,41,42,43,44,45,47,48	DC/DC POWER IC SOURCE	3V5V_EN	S0~S5
+15V	+15V	23,40,41,43,45,48	LARGE POWER	3V5V_EN	S0~S5
LANVCC	+3.3V	33	LAN POWER	LAN_ON	
5V_S5	+5V	11,27,28,48	PCH SUS POWER	S5_ON	S0~S3
3V_S5	+3.3V	3,7,8,9,10,11,29,38,48	Sys Management,PCH Resume Well, Intel HD Audio,USB,WLAN,WIMAX POWER	S5_ON	S0~S3
5VSUS	+5V	23,36,46,48	SLP_S4# CTRLD POWER	SUSON	S0~S3
3VSUS	+3.3V	15,38,48	SLP_S4# CTRLD POWER	SUSON	S0~S3
+1.5VSUS	+1.5V	3,5,11,13,14,41,45,48	DDR3 SODIMM POWER	SUSON	S0~S3
0.75VSMDDR_VTERM	+0.75V	13,14,41,48	DDR3 SODIMM REFERENCE POWER	MAINON	S0
+5V	+5V	7,11,18,22,23,24,25,26,32,35,36,38,48	SLP_S3# CTRLD POWER	MAINON	S0
+3V	+3.3V	7,8,9,10,11,13,14,18,22,23,24,25,26,27,29,30,31,32,34,35,37,38,40,41,42,44,45,46,48	SLP_S3# CTRLD POWER	MAINON	S0
+1.8V	+1.8V	5,8,11,,43,48	LVDS,NVM POWER	MAINON	S0
+1.5V	+1.5V	11,27,29,30,41	Mini PCIe,Express Card POWER	MAINON	S0
+1.05V	+1.05V	3,5,7,8,9,11,42,48	VTT POWER/PCH CORE POWER	MAINON	S0
+VGPU_CORE		18,45	VGA CORE POWER	GFX_ON	S0
+VCC_CORE		5,46,48	CPU CORE POWER	VRON	S0
+VCCSA		5,44,48	VCCSA POWER	MAINON	S0
LCDVCC	+3.3V	23	LCD Power	INT_LVDS_VDDEN	S0
+5V_HDD	+5V	29	HDD Power	MAINON	S0
BAT-V	+10V~+17V	47	MAIN BATTERY		S0~S5





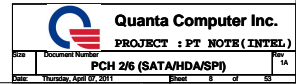


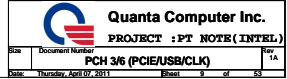
[illegible]

Pin Name	Strap description	Sampled	Configuration	Circuit
SPKR <i>Different from Calpella</i>	No reboot mode setting	PWROK	0 = Default (weak pull-down 20K) 1 = Setting to No-Reboot mode	
GNT3# / GPIO55	Top-board Swap Override	PWROK	0 = Default (weak pull-up 20K)	
INTRVMEN	Integrated 1.05V VRM enable	ALWAYS	Should be always pull-up	
HDA_SDO	Flash Descriptor Security Only for Interposer	PWROK	1 = Override 0 = Default (weak pull-up 20K)	
GNT1# / GPIO51	Boot BIOS Selection 1 [bit-1]	PWROK		
GPIO19 <i>Different from Calpella</i>	Boot BIOS Selection 0 [bit-0]	PWROK		
GNT2# / GPIO53	ESL strap (Server only)	PWROK	Should not be pull-down (weak pull-up 20K)	USE GPIO PIN
DF_TVS	DMI Termination voltage	PWROK	weak pull-down 20kohm	
HDA_SYNC	On-Die PLL VR Voltage Select	RSMRST	(A) Supported 1.0V (weak pull-down) 1 = Supported by 1.8V	
GPIO15				
GPIO28 <i>Different from Calpella</i>	On-die PLL Voltage Regulator	RSMRST#	0 = Disable 1 = Enable (Default)	
	DSW VRMEN(page7)		0 = Disable 1 = Enable	

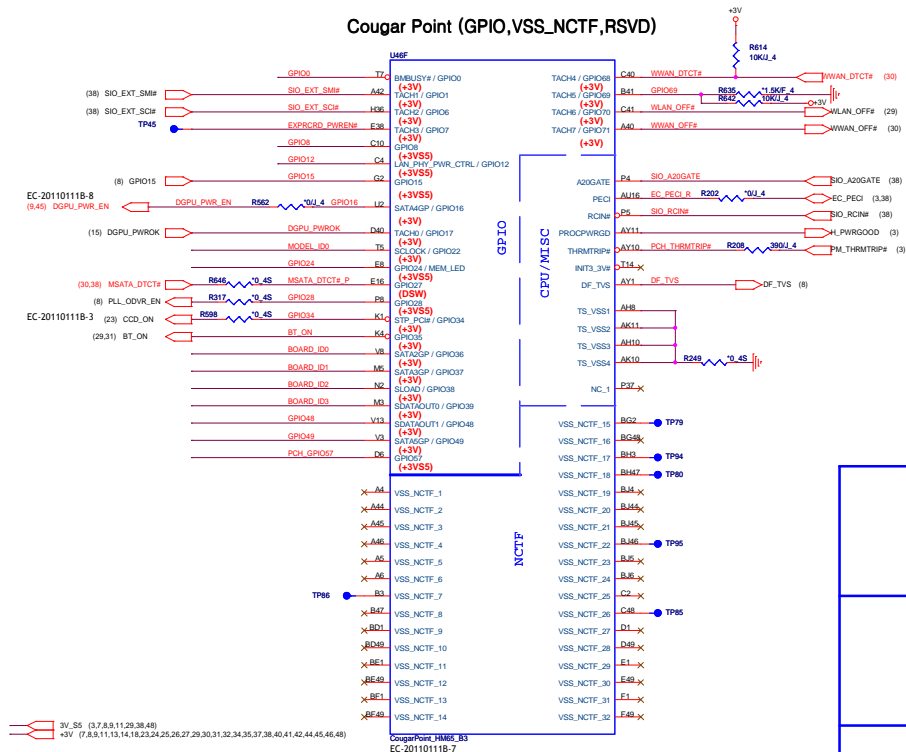
[illegible]

TPM Function	R420
Enable	Stuff
Disable	NC (Default)

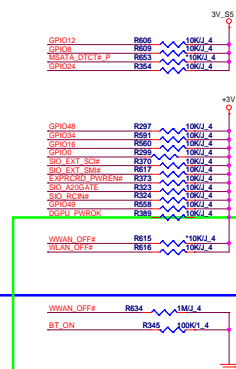
[illegible]



Cougar Point (GPIO,VSS_NCTF,RSVD)

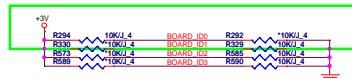


GPIO Pull-up/Pull-down(CLG)

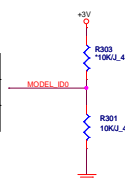


BOARD ID SETTING

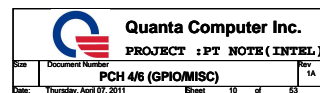
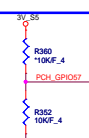
Board ID For Function	ID3 GPIO39	ID2 GPIO38	ID1 GPIO37	ID0 GPIO36
SDV	0	0	0	0
DV2	0	0	0	1
SIT	0	0	1	0
SVT	0	0	1	1
SOVP	0	1	0	0



Model ID	MODEL_ID0
14°	0
15°	1



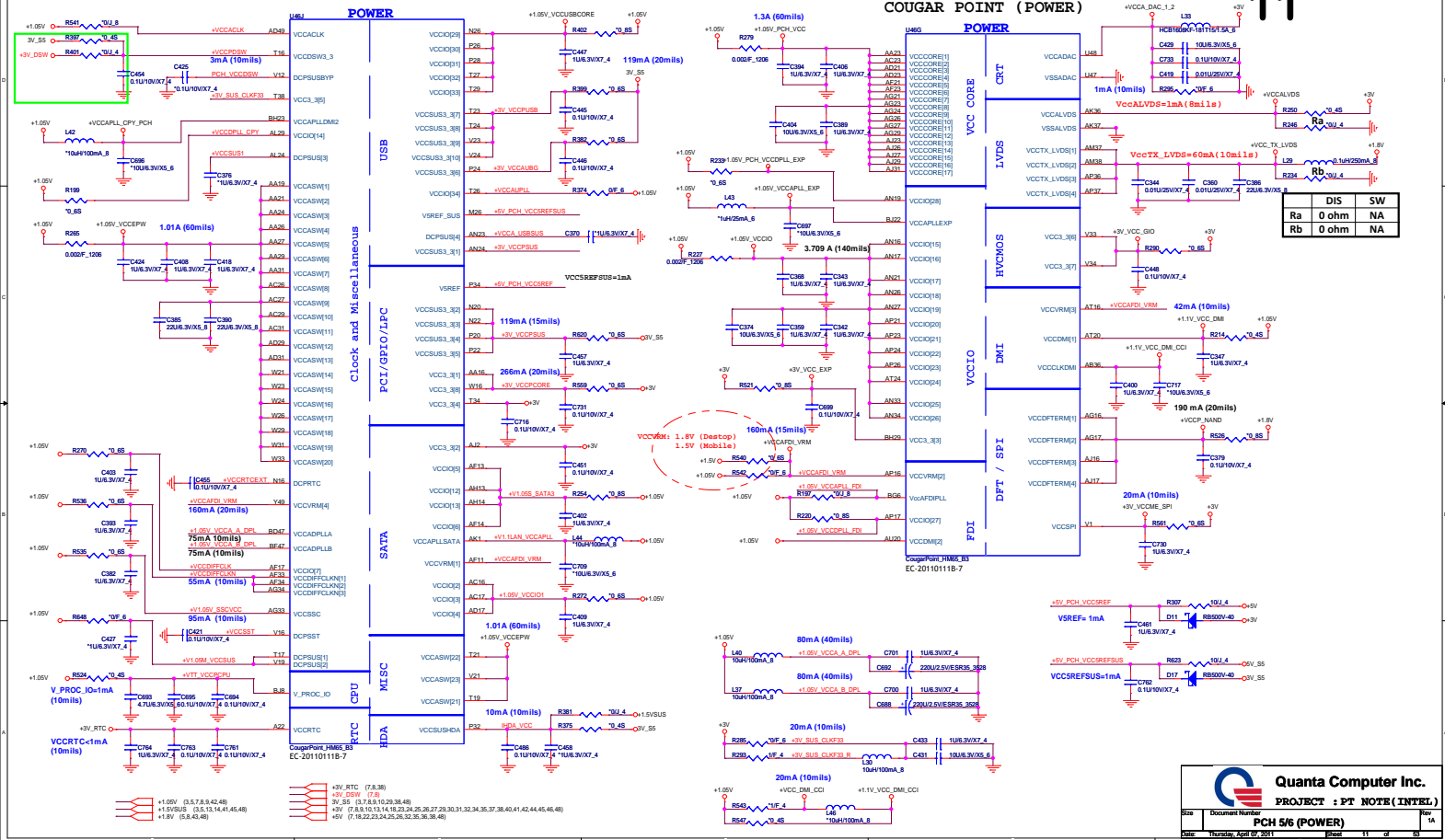
TPM physical presence	
PCH_GPIO57	Low: Default




Cougar Point-M (POWER)

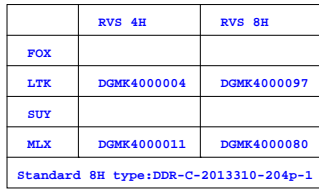
COUGAR POINT (POWER)


11





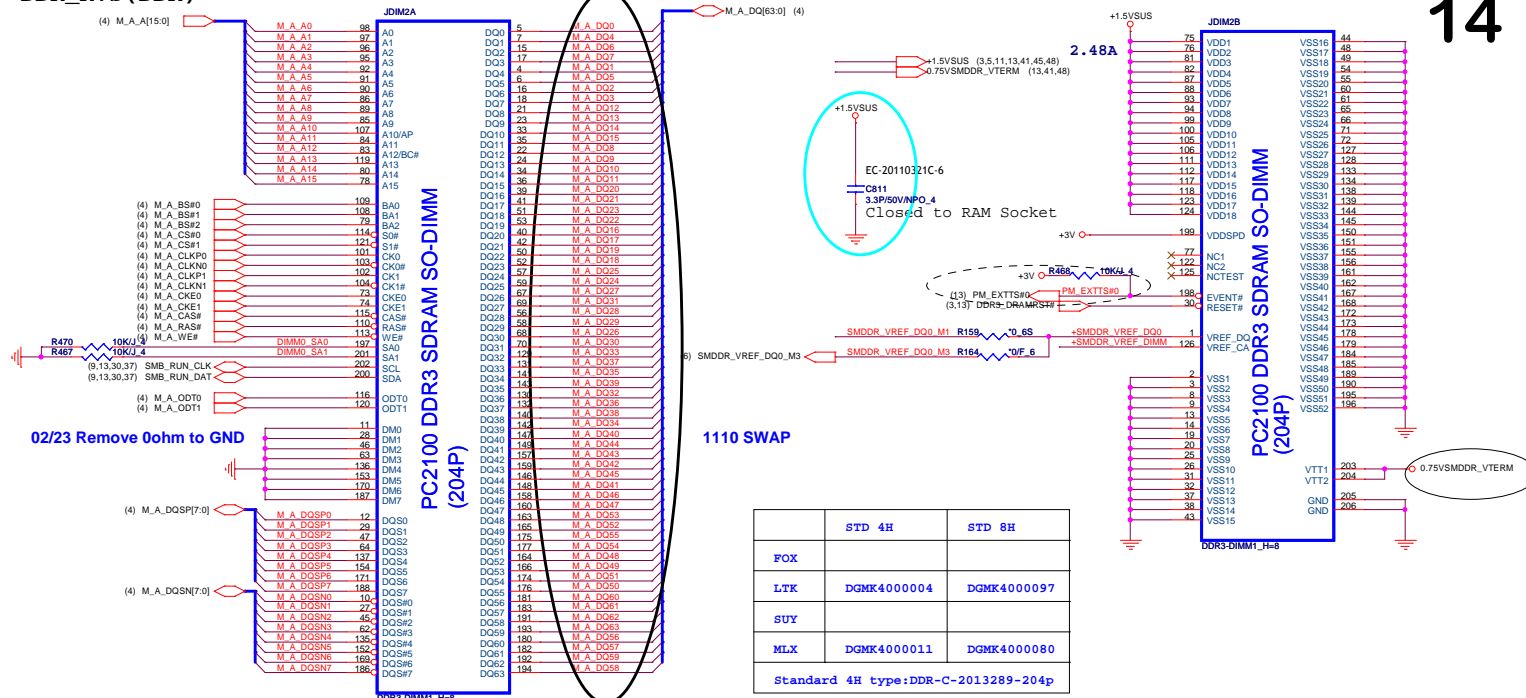
 Quanta Computer Inc. PROJECT : PT NOTE (INTEL)		
Size	Document Number	Rev
	PCH 6/6 (GND)	1A
Date:	Thursday, April 07, 2011	Sheet 12 of 53



 Quanta Computer Inc. PROJECT : PT NOTE (INTEL)		
Size	Document Number	Rev
	DDRIII SO-DIMM-0	1A
Date:	Friday, March 25, 2011	Sheet 13 of 53

DDR_RVS (DDR)

14



VREF DQ0 M2 Solution

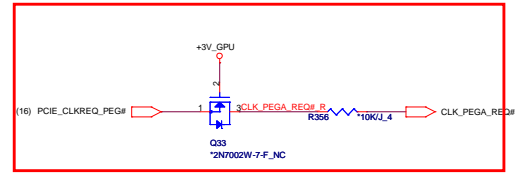
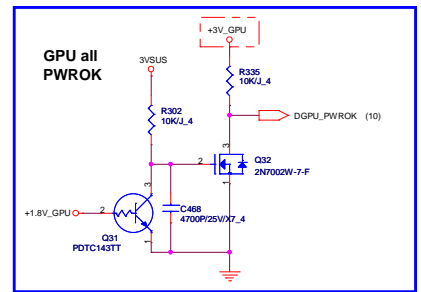
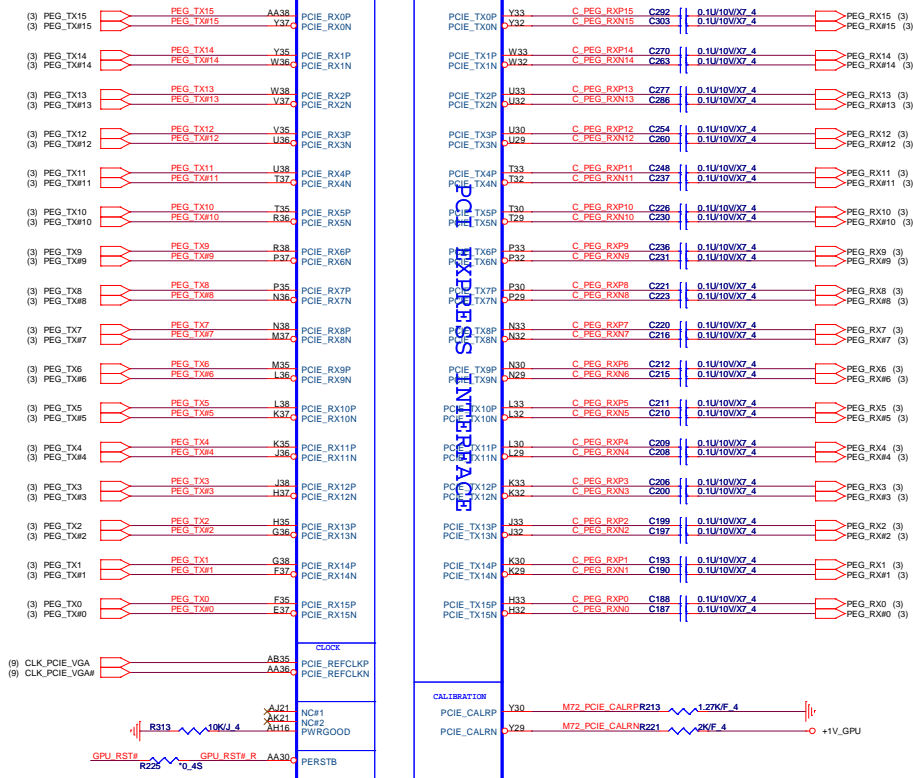
Place these Caps near So-Dimm0.

VREF DQ0 M1 Solution

Quanta Computer Inc.
PROJECT : PT NOTE (INTEL)
DDR3 SO-DIMM-1

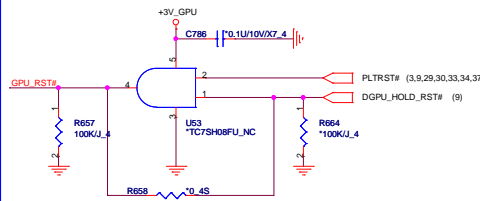
Size: Document Number: Rev: 1A
 Date: Friday, March 25, 2011 Sheet: 14 of 53

2.5GT/s bit rate



+1V_GPU
+3V_GPU
+1.8V_GPU
+3V_GPU

DGPU RST# (CLG)



Quanta Computer Inc.
PROJECT : PT NOTE (INTEL)
Size Document Number
N11M-GE2 (PCIE VF) 1/5
Date Thursday, April 07, 2011 Sheet 15 of 53

32-bit GPU

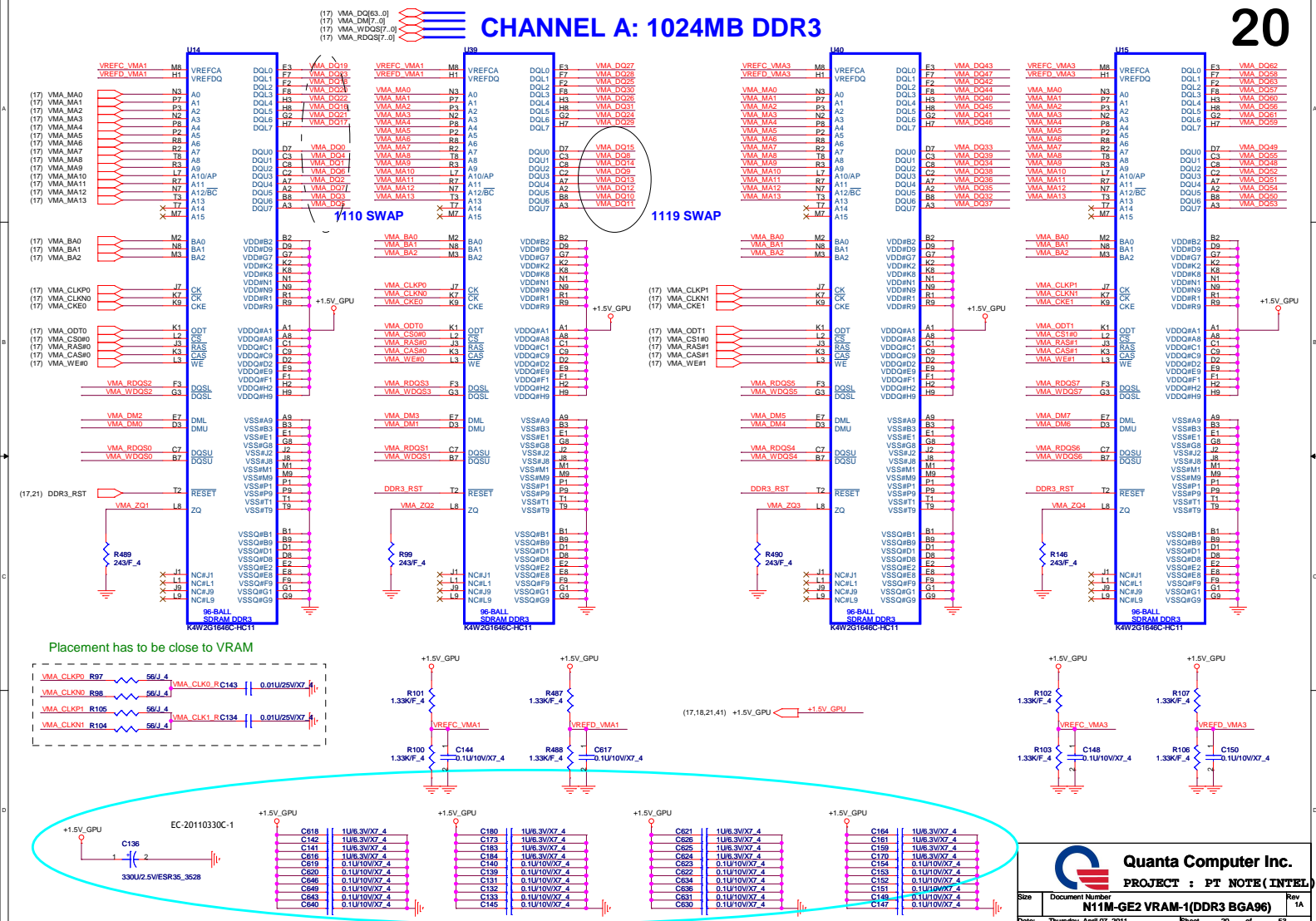
0010 001F 0020 002F

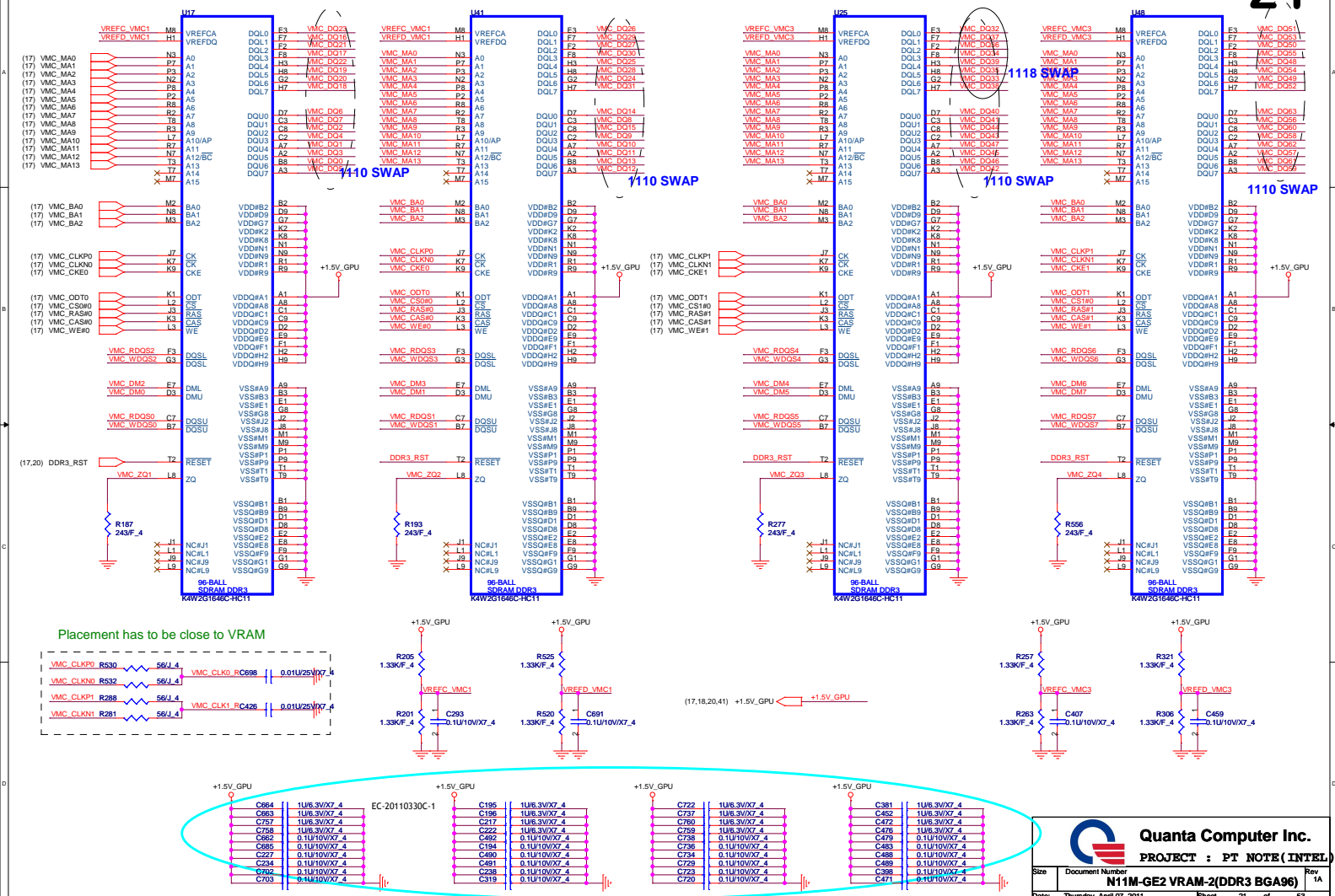
EXT. LOGIC BLOCK

TEMP. FPG

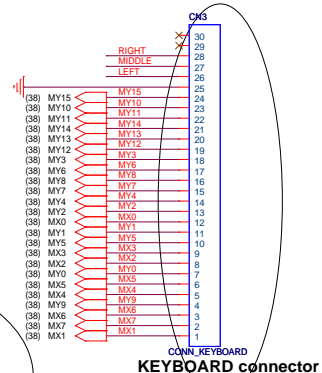
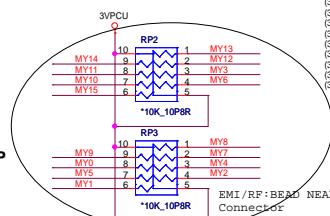
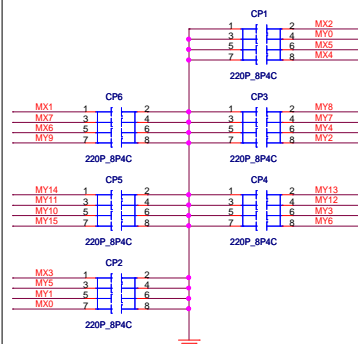
The diagram is a complex PCB layout for a graphics card, showing various components and their electrical connections. Key features include:

- VRAM TYPE:** A section at the top left showing a 1.8V GPU VRAM type with components like R204, R205, R206, R207, R208, R209, R210, R211, R212, R213, R214, R215, R216, R217, R218, R219, R220, R221, R222, R223, R224, R225, R226, R227, R228, R229, R230, R231, R232, R233, R234, R235, R236, R237, R238, R239, R240, R241, R242, R243, R244, R245, R246, R247, R248, R249, R250, R251, R252, R253, R254, R255, R256, R257, R258, R259, R260, R261, R262, R263, R264, R265, R266, R267, R268, R269, R270, R271, R272, R273, R274, R275, R276, R277, R278, R279, R280, R281, R282, R283, R284, R285, R286, R287, R288, R289, R290, R291, R292, R293, R294, R295, R296, R297, R298, R299, R300, R301, R302, R303, R304, R305, R306, R307, R308, R309, R310, R311, R312, R313, R314, R315, R316, R317, R318, R319, R320, R321, R322, R323, R324, R325, R326, R327, R328, R329, R330, R331, R332, R333, R334, R335, R336, R337, R338, R339, R340, R341, R342, R343, R344, R345, R346, R347, R348, R349, R350, R351, R352, R353, R354, R355, R356, R357, R358, R359, R360, R361, R362, R363, R364, R365, R366, R367, R368, R369, R370, R371, R372, R373, R374, R375, R376, R377, R378, R379, R380, R381, R382, R383, R384, R385, R386, R387, R388, R389, R390, R391, R392, R393, R394, R395, R396, R397, R398, R399, R400, R401, R402, R403, R404, R405, R406, R407, R408, R409, R410, R411, R412, R413, R414, R415, R416, R417, R418, R419, R420, R421, R422, R423, R424, R425, R426, R427, R428, R429, R430, R431, R432, R433, R434, R435, R436, R437, R438, R439, R440, R441, R442, R443, R444, R445, R446, R447, R448, R449, R450, R451, R452, R453, R454, R455, R456, R457, R458, R459, R460, R461, R462, R463, R464, R465, R466, R467, R468, R469, R470, R471, R472, R473, R474, R475, R476, R477, R478, R479, R480, R481, R482, R483, R484, R485, R486, R487, R488, R489, R490, R491, R492, R493, R494, R495, R496, R497, R498, R499, R500, R501, R502, R503, R504, R505, R506, R507, R508, R509, R510, R511, R512, R513, R514, R515, R516, R517, R518, R519, R520, R521, R522, R523, R524, R525, R526, R527, R528, R529, R530, R531, R532, R533, R534, R535, R536, R537, R538, R539, R540, R541, R542, R543, R544, R545, R546, R547, R548, R549, R550, R551, R552, R553, R554, R555, R556, R557, R558, R559, R560, R561, R562, R563, R564, R565, R566, R567, R568, R569, R570, R571, R572, R573, R574, R575, R576, R577, R578, R579, R580, R581, R582, R583, R584, R585, R586, R587, R588, R589, R590, R591, R592, R593, R594, R595, R596, R597, R598, R599, R600, R601, R602, R603, R604, R605, R606, R607, R608, R609, R610, R611, R612, R613, R614, R615, R616, R617, R618, R619, R620, R621, R622, R623, R624, R625, R626, R627, R628, R629, R630, R631, R632, R633, R634, R635, R636, R637, R638, R639, R640, R641, R642, R643, R644, R645, R646, R647, R648, R649, R650, R651, R652, R653, R654, R655, R656, R657, R658, R659, R660, R661, R662, R663, R664, R665, R666, R667, R668, R669, R670, R671, R672, R673, R674, R675, R676, R677, R678, R679, R680, R681, R682, R683, R684, R685, R686, R687, R688, R689, R690, R691, R692, R693, R694, R695, R696, R697, R698, R699, R700, R701, R702, R703, R704, R705, R706, R707, R708, R709, R710, R711, R712, R713, R714, R715, R716, R717, R718, R719, R720, R721, R722, R723, R724, R725, R726, R727, R728, R729, R730, R731, R732, R733, R734, R735, R736, R737, R738, R739, R740, R741, R742, R743, R744, R745, R746, R747, R748, R749, R750, R751, R752, R753, R754, R755, R756, R757, R758, R759, R760, R761, R762, R763, R764, R765, R766, R767, R768, R769, R770, R771, R772, R773, R774, R775, R776, R777, R778, R779, R780, R781, R782, R783, R784, R785, R786, R787, R788, R789, R790, R791, R792, R793, R794, R795, R796, R797, R798, R799, R800, R801, R802, R803, R804, R805, R806, R807, R808, R809, R810, R811, R812, R813, R814, R815, R816, R817, R818, R819, R820, R821, R822, R823, R824, R825, R826, R827, R828, R829, R830, R831, R832, R833, R834, R835, R836, R837, R838, R839, R840, R841, R842, R843, R844, R845, R846, R847, R848, R849, R850, R851, R852, R853, R854, R855, R856, R857, R858, R859, R860, R861, R862, R863, R864, R865, R866, R867, R868, R869, R870, R871, R872, R873, R874, R875, R876, R877, R878, R879, R880, R881, R882, R883, R884, R885, R886, R887, R888, R889, R890, R891, R892, R893, R894, R895, R896, R897, R898, R899, R900, R901, R902, R903, R904, R905, R906, R907, R908, R909, R910, R911, R912, R913, R914, R915, R916, R917, R918, R919, R920, R921, R922, R923, R924, R925, R926, R927, R928, R929, R930, R931, R932, R933, R934, R935, R936, R937, R938, R939, R940, R941, R942, R943, R944, R945, R946, R947, R948, R949, R950, R951, R952, R953, R954, R955, R956, R957, R958, R959, R960, R961, R962, R963, R964, R965, R966, R967, R968, R969, R970, R971, R972, R973, R974, R975, R976, R977, R978, R979, R980, R981, R982, R983, R984, R985, R986, R987, R988, R989, R990, R991, R992, R993, R994, R995, R996, R997, R998, R999, R1000, R1001, R1





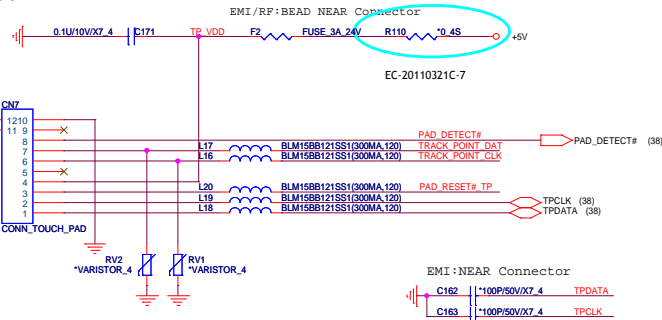
1122 SWAP



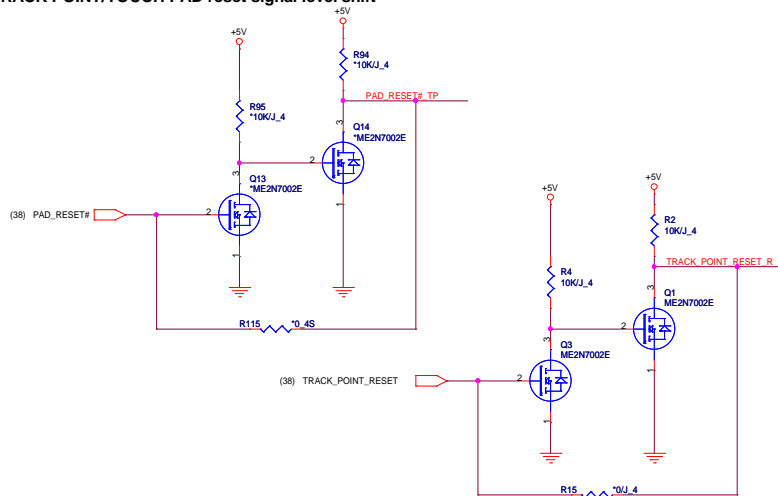
KEYBOARD connector

1122 SWAP pin define

TOUCH PAD



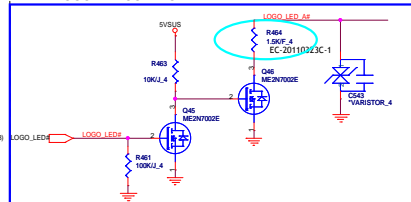
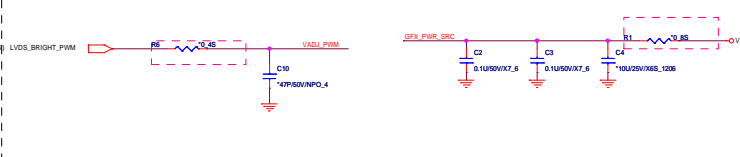
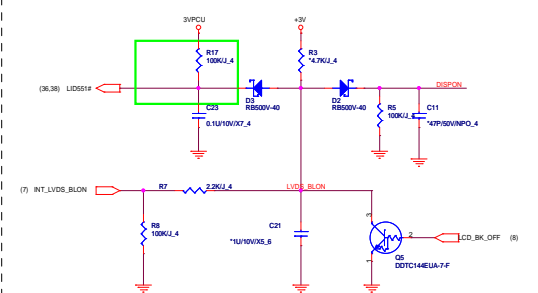
TRACK POINT/TOUCH PAD reset signal level shift



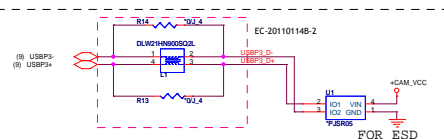
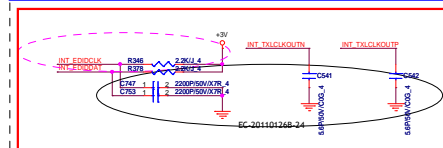
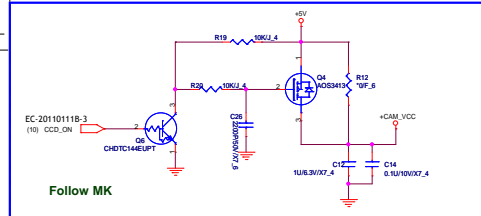
CONN

CONT

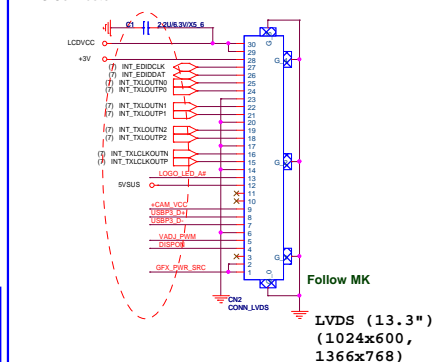
Back light



CAMERA VCC Control

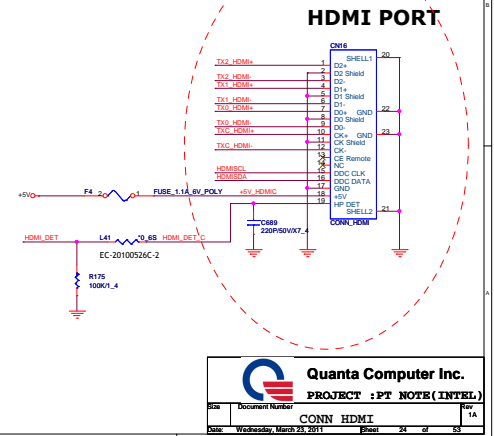


LVDS Connector

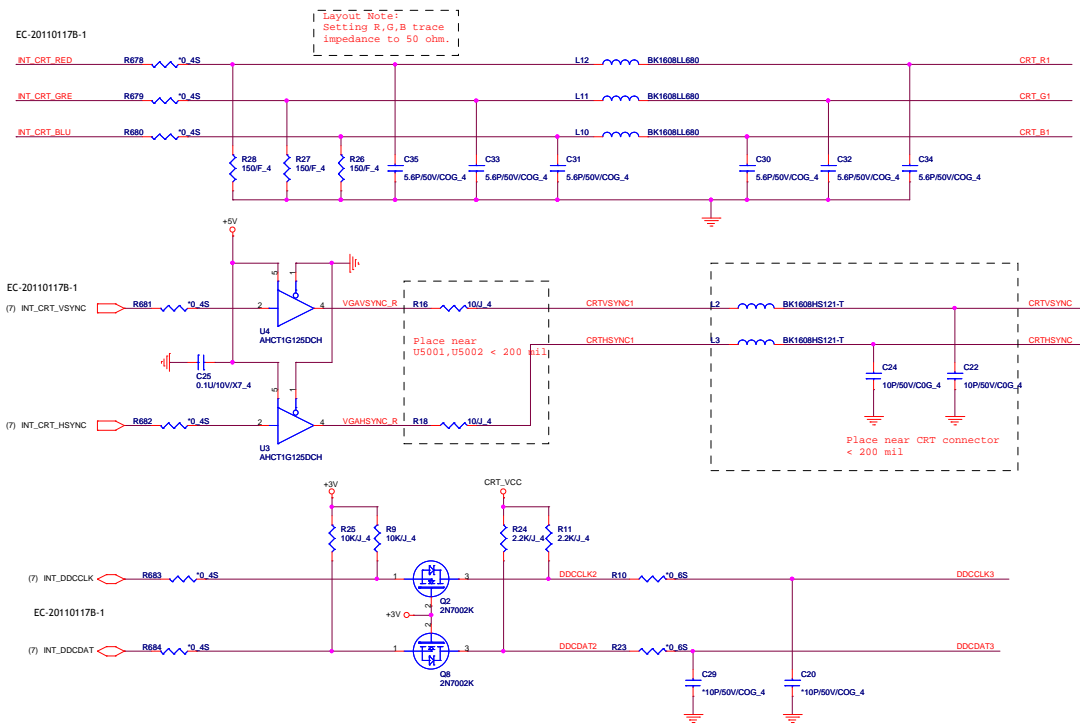


Follow MK

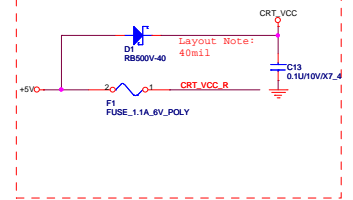
LVDS (13.3")
(1024x600,
1366x768)



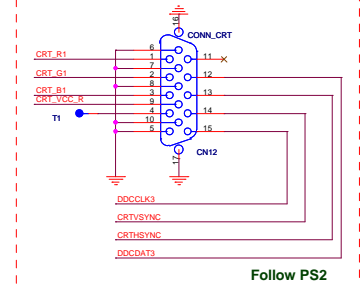
(7) INT_CRT_RED
(7) INT_CRT_GRE
(7) INT_CRT_BLU



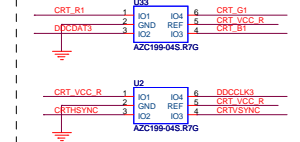
CRT Power



CRT Connector



ESD Reserve



Quanta Computer Inc.

PROJECT :PT NOTE(INTEL)

Doc: Wednesday, March 23, 2011 Sheet 25 of 55

CRT CONN

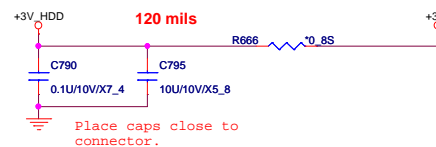
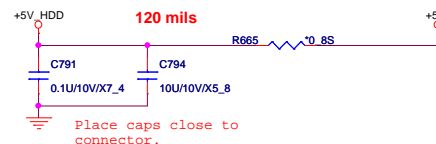
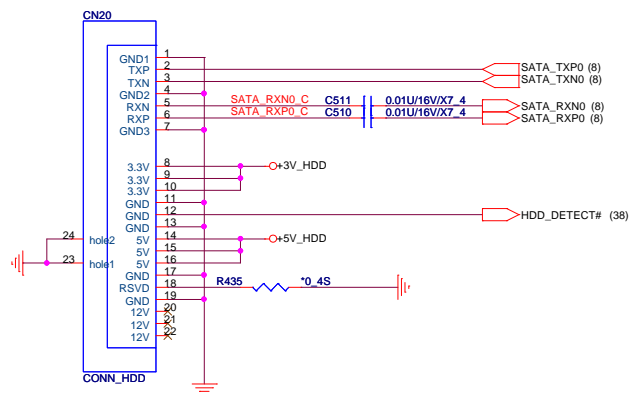
Rev 1A

SATA Connector.

(7,11,18,22,23,24,25,32,35,36,38,48) +5V
(7,8,9,10,11,13,14,18,23,24,25,27,29,30,31,32,34,35,37,38,40,41,42,44,45,46,48) +3V

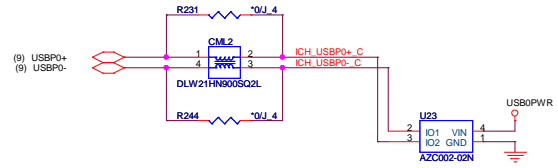
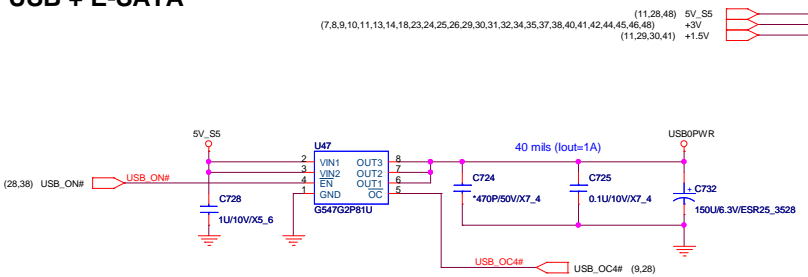


26

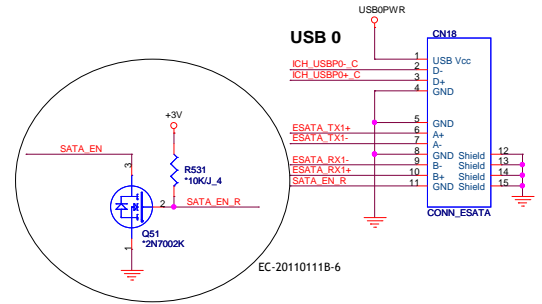
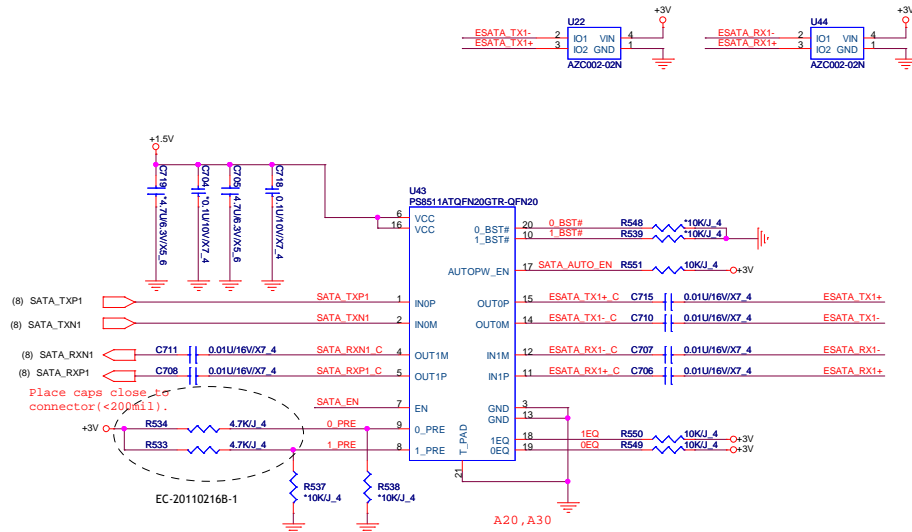


Quanta Computer Inc.
PROJECT : PT NOTE(INTEL)

Size	Document Number	Rev
	SATA HDD	1A
Date:	Wednesday, March 23, 2011	Sheet 26 of 53




E-SATA RE-DRIVER



All straps of PS8511A have int. PL 150Kohm.

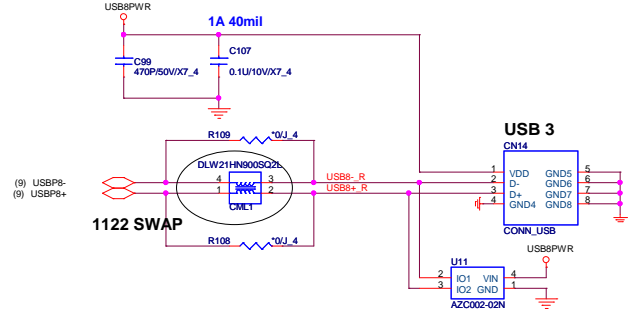
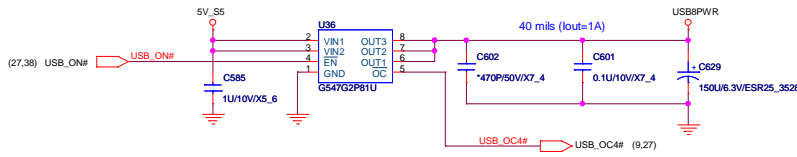
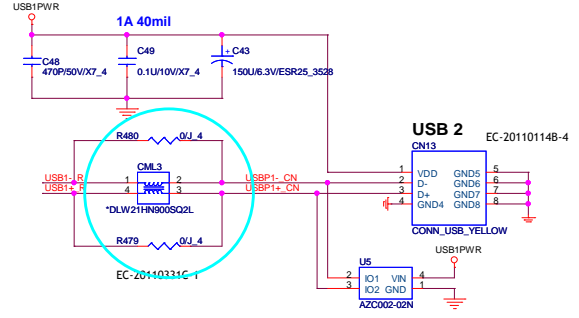
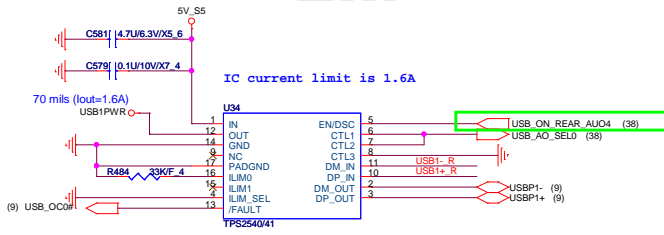
EN	AUTO_EN	0/1EQ	0/1EQ	0/1_BST#	0/1_BST#	0/1_PRE	0/1_PRE	Function
0	X	X	X	X	X	X	X	Standby
1	0	X	X	X	X	X	X	disable auto power saving
1	1	X	X	X	X	X	X	enable auto power saving
1	X	0	X	X	X	X	X	Short and medium length
1	X	X	1	X	X	X	X	Long length
1	X	X	X	0	X	X	X	Output :800~1200 mVpp
1	X	X	X	X	1	X	X	Output :400~700 mVpp
1	X	X	X	X	X	0	X	Pre-emphasis disabled
1	X	X	X	X	X	X	1	Pre-emphasis enabled

 Quanta Computer Inc. PROJECT : PT NOTE (INTEL)		
Size	Document Number	Rev
	USB X1/USB+ESATA	1A
Date:	Friday, April 01, 2011	Sheet 27 of 53

TPS2541 table

CTL1	CTL2	CTL3	Mode
0	0	X	Dedicated Charging Port, Auto-detect
0	1	X	Dedicated Charging Port, BC Specification 1.1 Only
1	0	X	Dedicated Charging Port, Apple Only
1	1	0	Standard Downstream Port, USB 2.0 Mode
1	1	1	Charging Downstream Port, BC Specification 1.1

Table 3 - TPS2541 Control Truth Table



Quanta Computer Inc.
PROJECT : PT NOTE (INTEL)

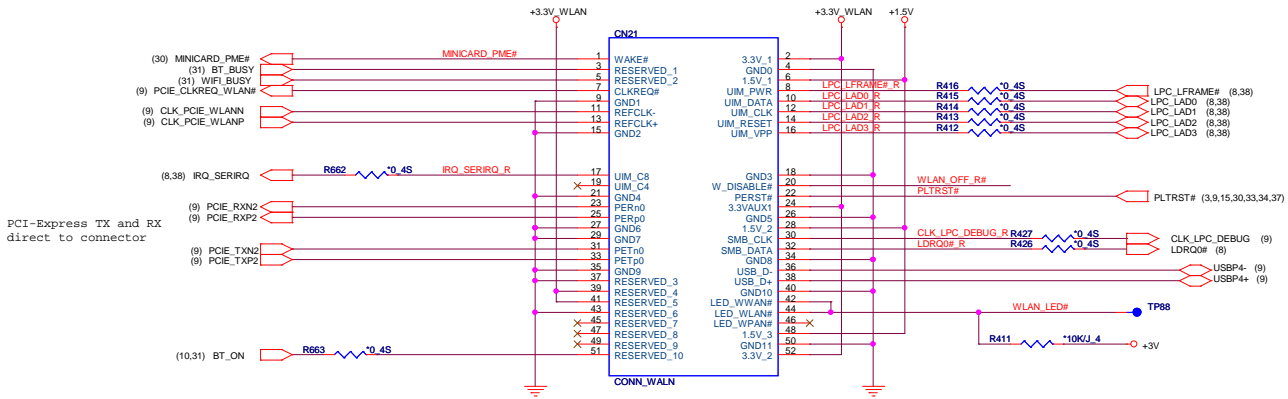
Size: Document Number: **CONN USB X2** Rev: 1A

Date: Thursday, March 31, 2011 Sheet: 28 of 53

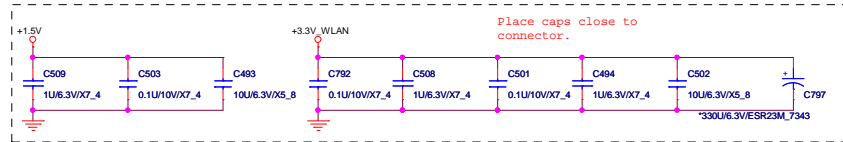
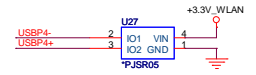
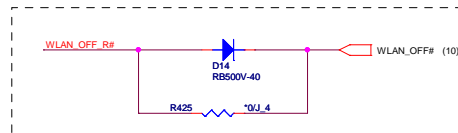
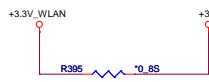
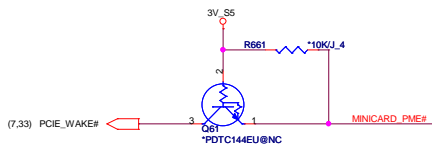
MiniCard WLAN/WiMAX connector

(7,8,9,10,11,13,14,18,23,24,25,26,27,30,31,32,34,35,37,38,40,41,42,44,45,46,48) +3V
(11,27,30,41) +1.5V
(3,7,8,9,10,11,38,48) 3V_S5

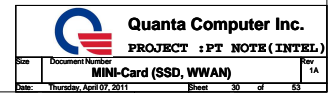
29



PCI-Express TX and RX
direct to connector



Quanta Computer Inc.
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Size Document Number **MINI-Card (WLAN/Wimax)** Rev 1A
Date: Wednesday, March 23, 2011 Sheet 29 of 53



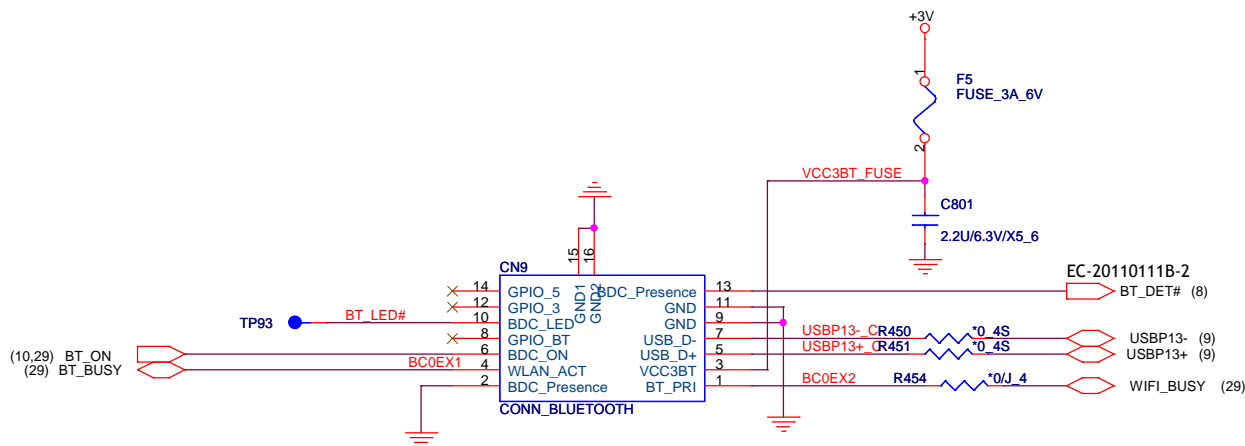
BLUETOOTH

(7,8,9,10,11,13,14,18,23,24,25,26,27,29,30,32,34,35,37,38,40,41,42,44,45,46,48)

+3V

+3V

31

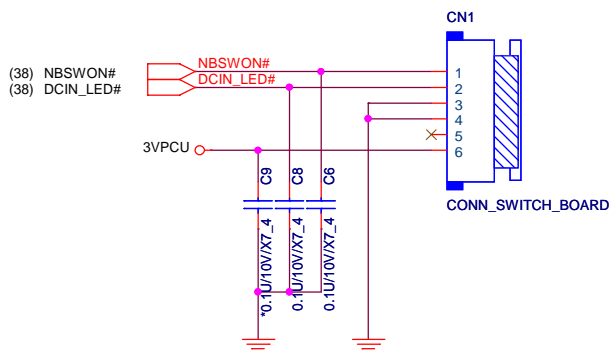


Connect to S/B

(7,8,22,23,33,36,38,40,43,45,47,48)

3VPCU

3VPCU



Quanta Computer Inc.

PROJECT : PT NOTE (INTEL)

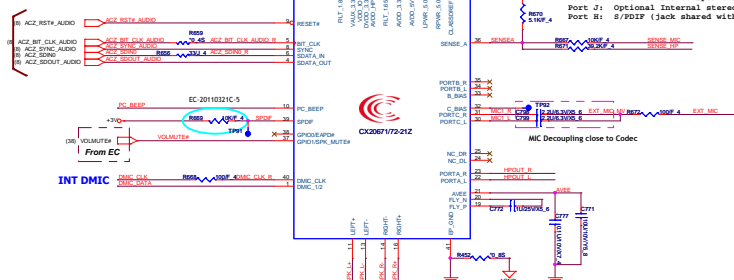
Size	Document Number	Rev
	BLUETOOTH	1A
Date:	Thursday, April 07, 2011	Sheet 31 of 53

1. The VDD_ID and VALX_3.3 pins should be connected to same power supply domain as HDA bus controller so that the HDA controller and codec bus interface will power up at the same time. This will avoid bus leakage issues if using HDA controller with bus pull-up strap options. See other FET option on this page if these supplies are not on same domain as HDA controller.
2. To support Wake-on-Jack, the codec VALX_3.3 pin must be connected from a Standby supply.
3. C309, C310, C311 are optional.

Do not install unless needed for EMI/SL.

Notes: R37 only needed if supply to VALX_3.3 is removed during system in-idle.

HD Audio Bus



Port Configuration

Port A: Headphone jack (jack shared with S/PDIF)
 Port B: Internal analog mono or stereo MIC.
 Port C: Microphone Jack
 Port D: Internal stereo speakers
 Port E: Optional internal stereo digital mic
 Port F: S/PDIF (jack shared with headphone)

Layout Note: Path from +5V to LP1WR_5.0 and RP1WR_5.0 must be very low resistance (<0.01 ohms). Place bypass caps very close to device.

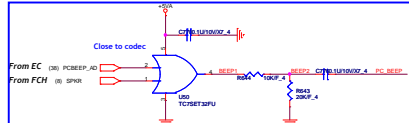
0.1A or 2.5A??

For EMI, close the audio I/O connector.

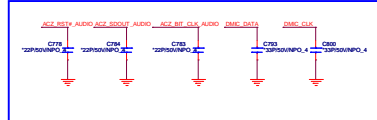
EMI Reserved Please see Design Guide for audio grounding.



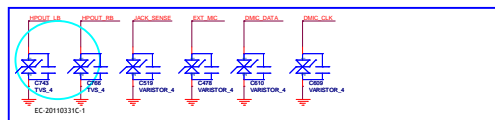
PC BEEP CONTROL



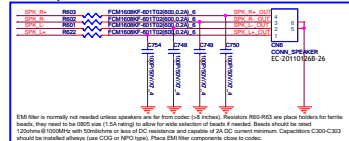
FOR EMI Reserve



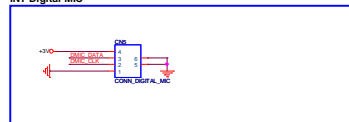
ESD Reserve



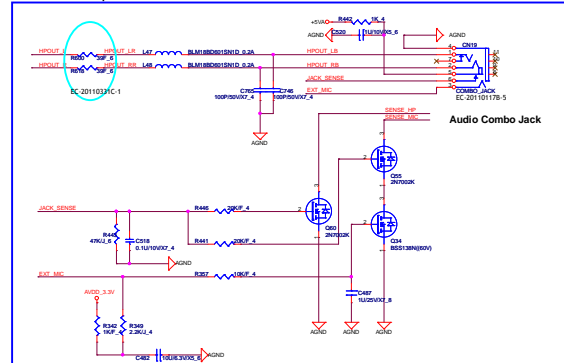
Internal Speaker



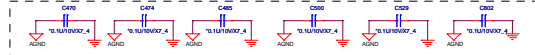
INT Digital MIC

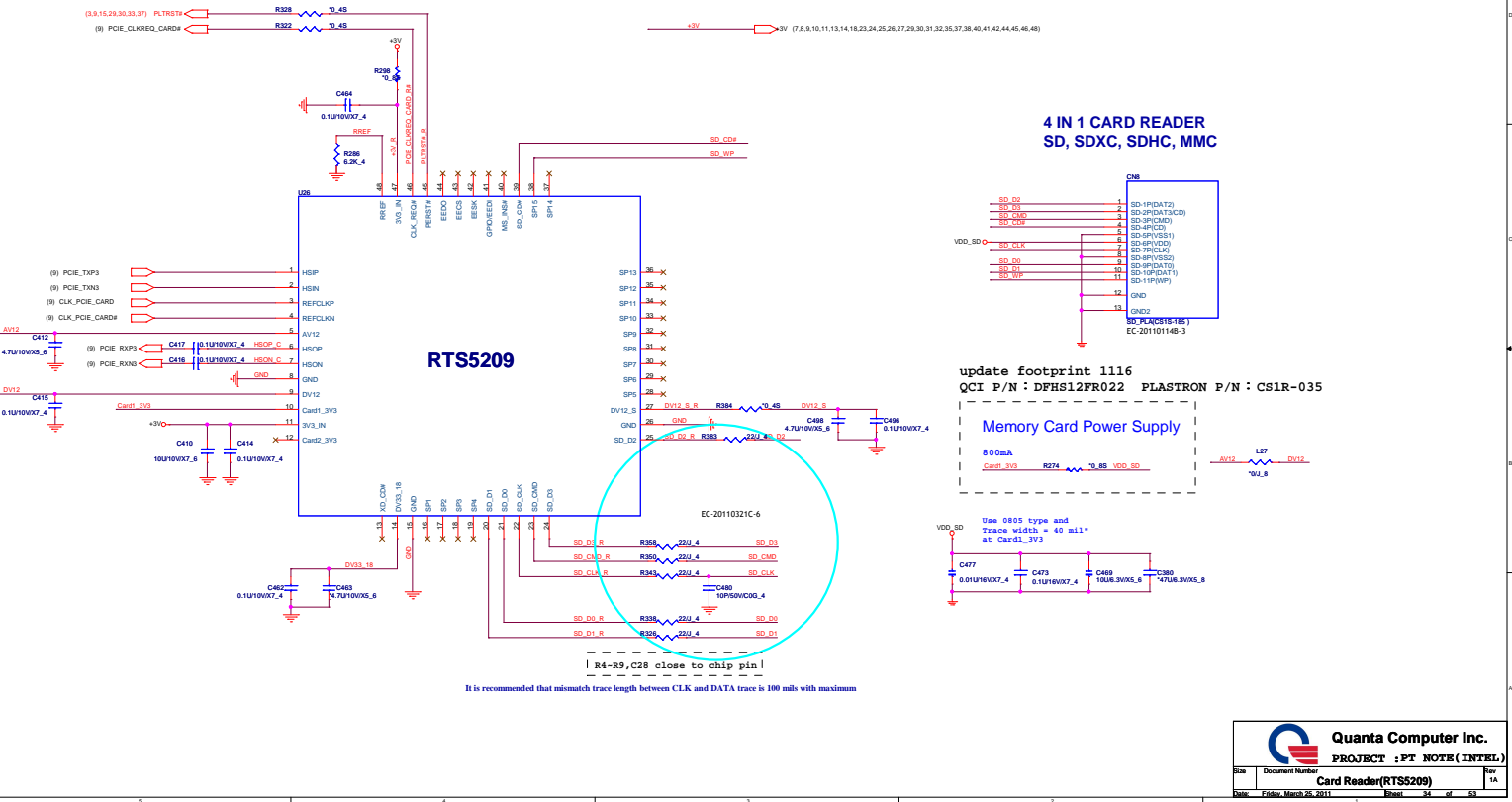


External MIC/Headphone Combo

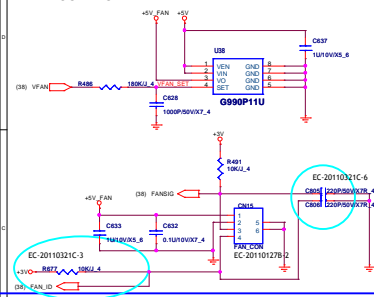


EMI Reserve

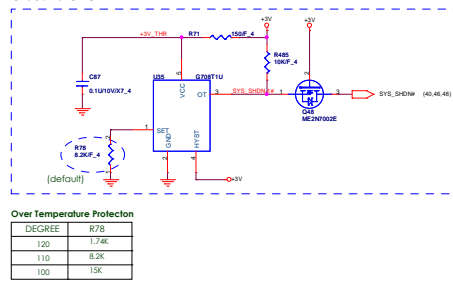




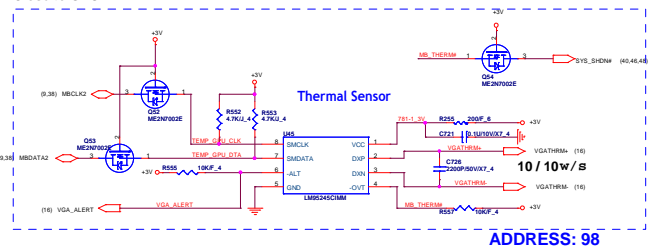
FAN CONTROL



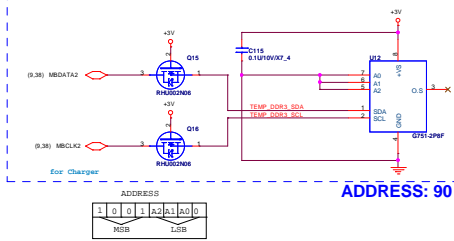
Close to CPU



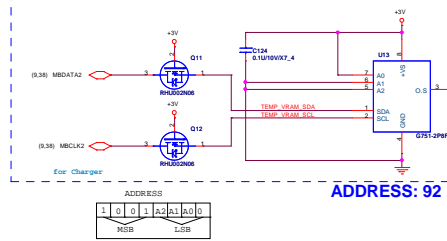
Close to GPU



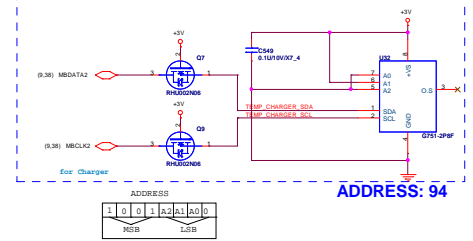
Close to DDR3 Socket



Close to VRAM

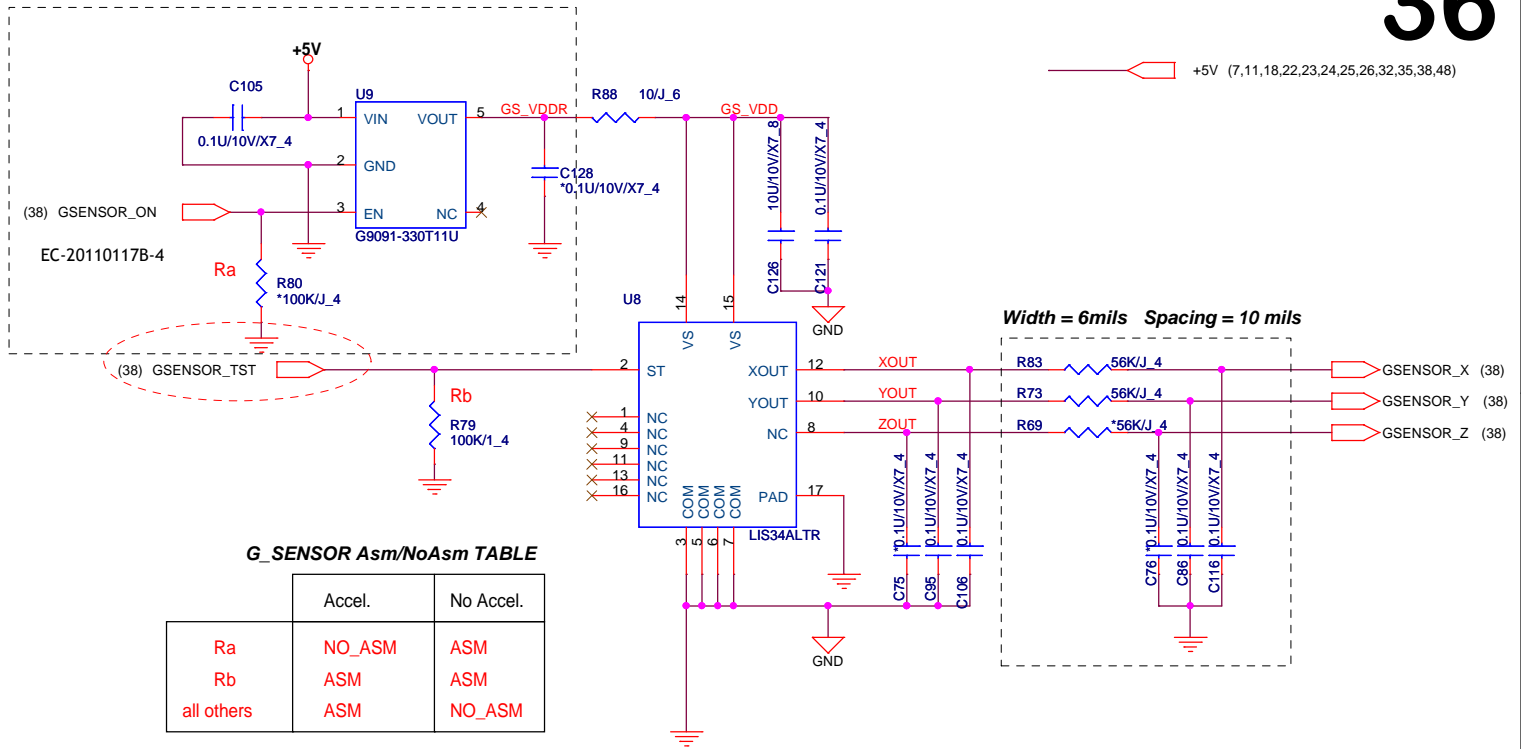


Close to Power Charger

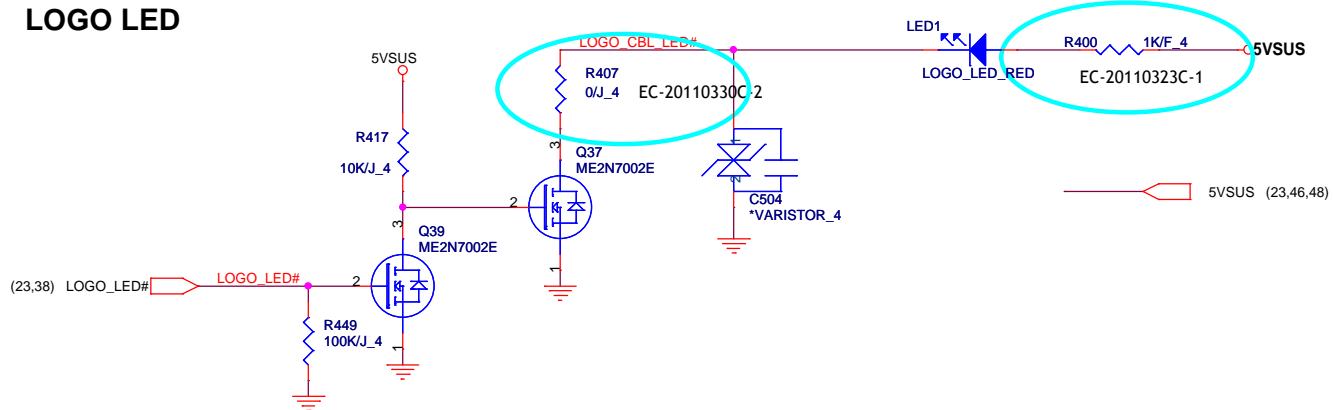


Accelerometer Sensor

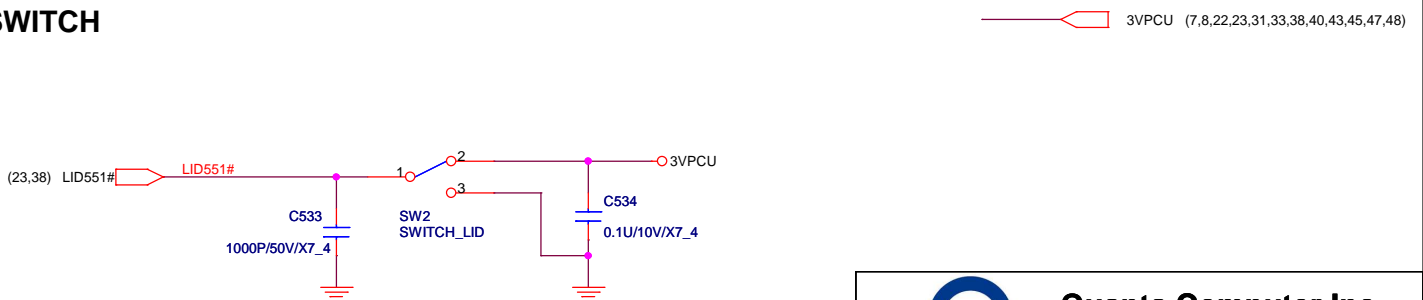
36



LOGO LED



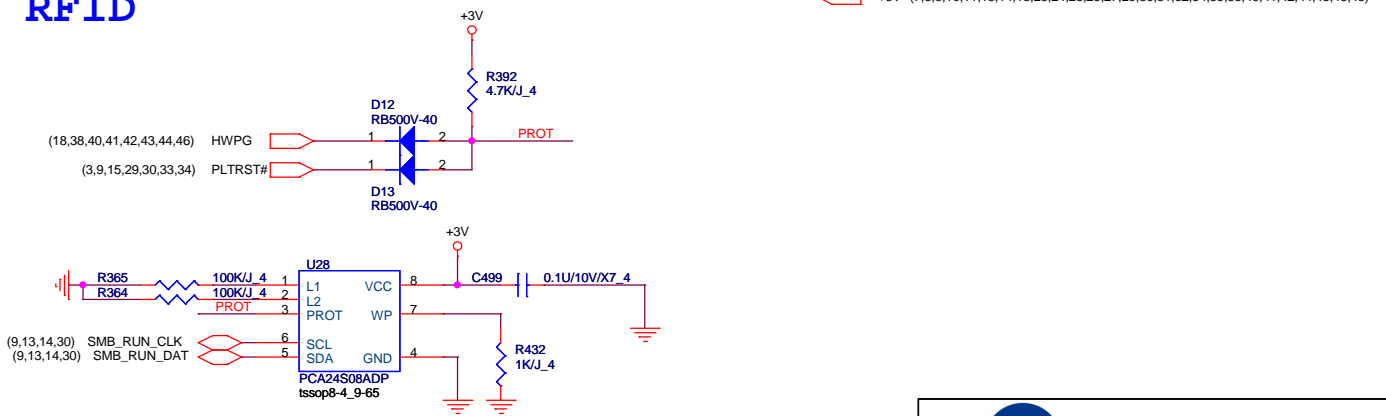
LID SWITCH




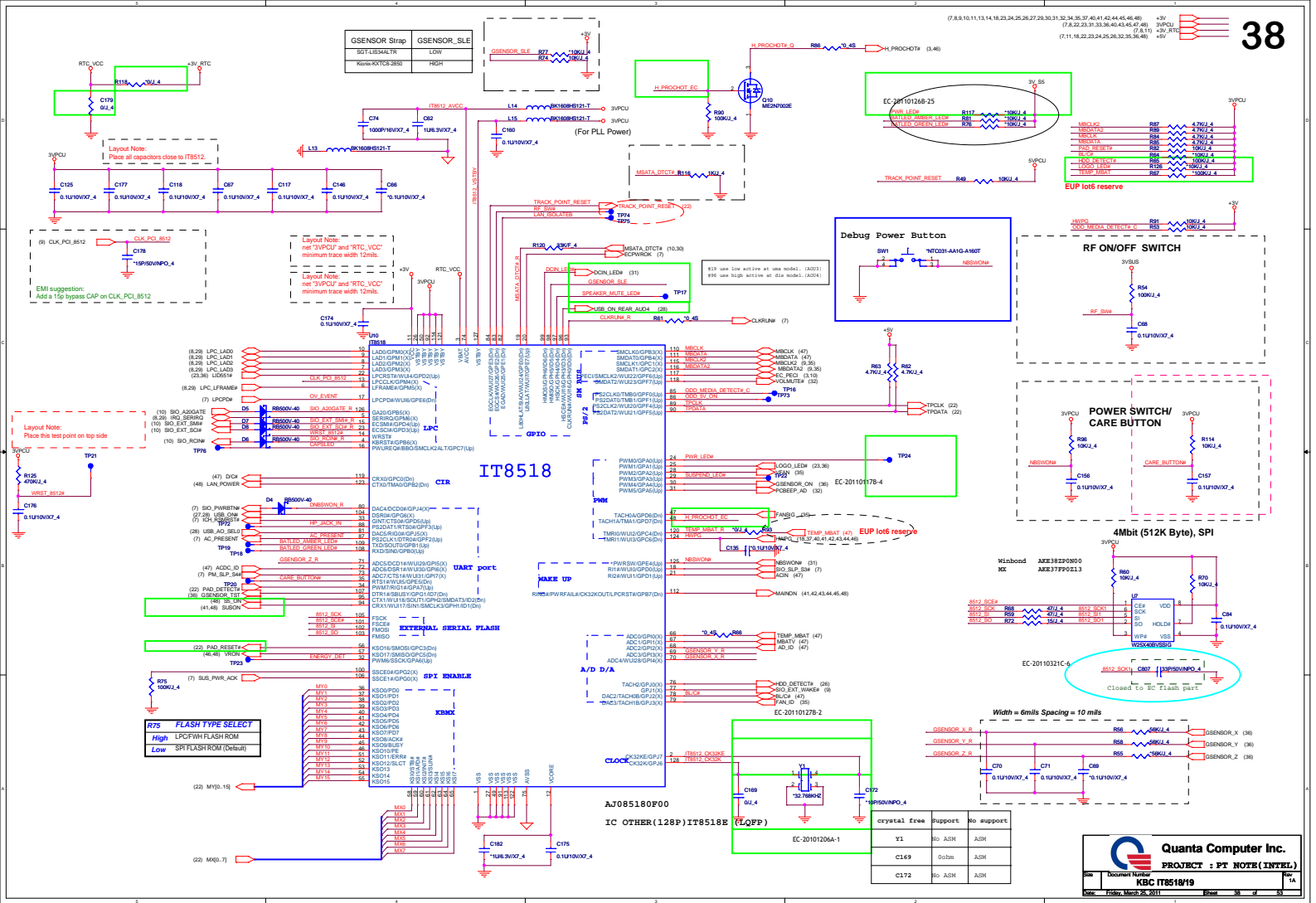
Quanta Computer Inc.
PROJECT : PT NOTE (INTEL)

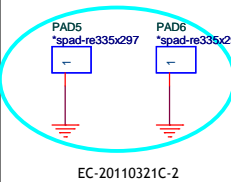
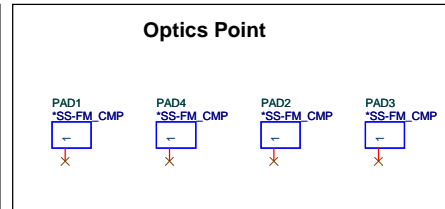
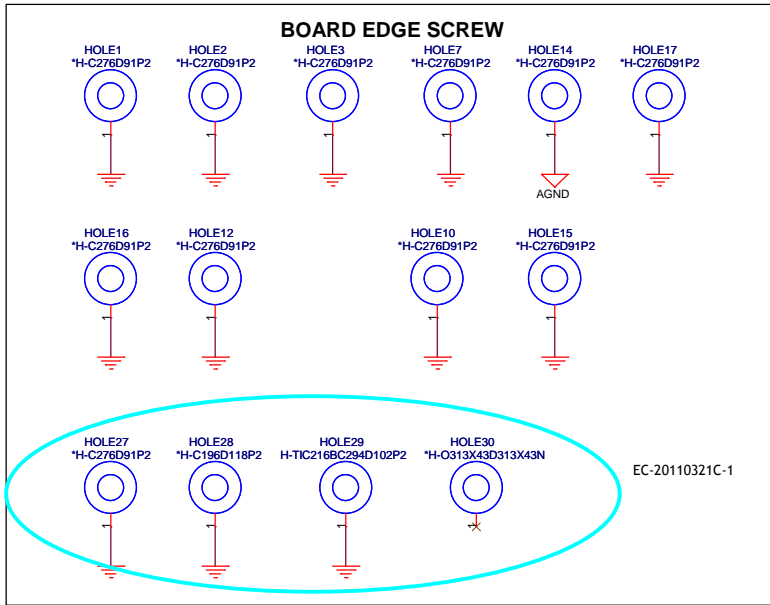
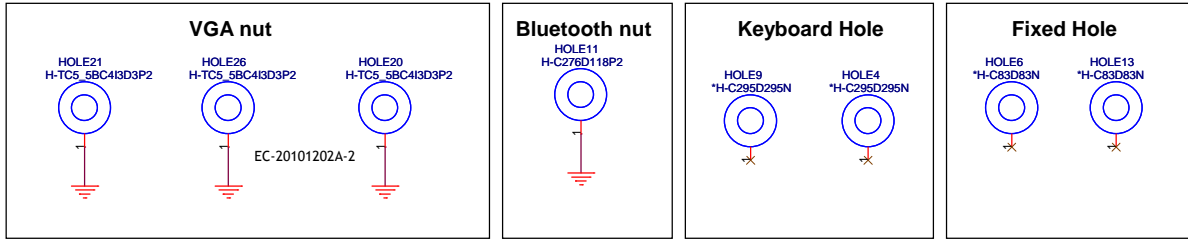
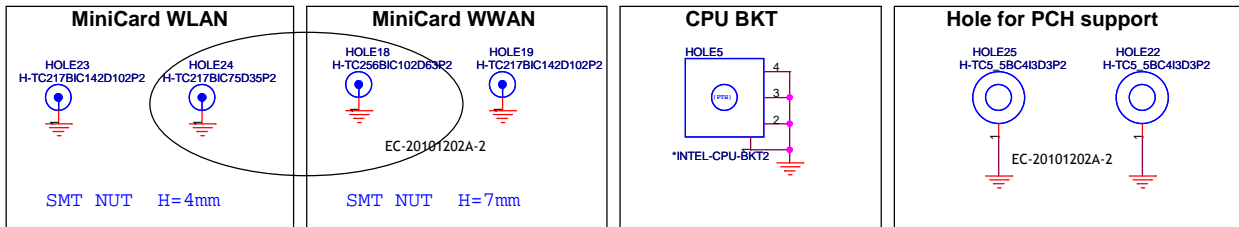
Size	Document Number	Rev
	G-SENSOR/LID SWITCH/LED	1A
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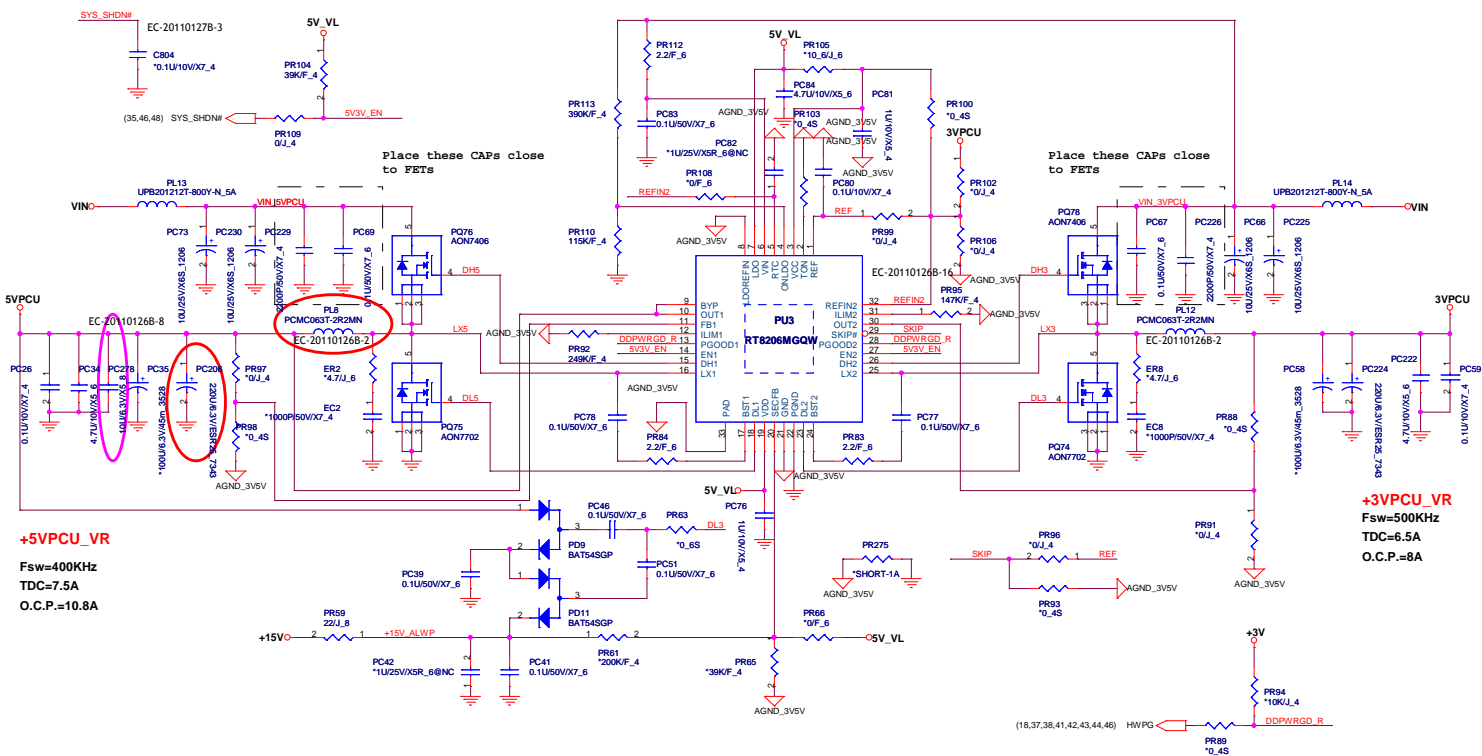
RFID

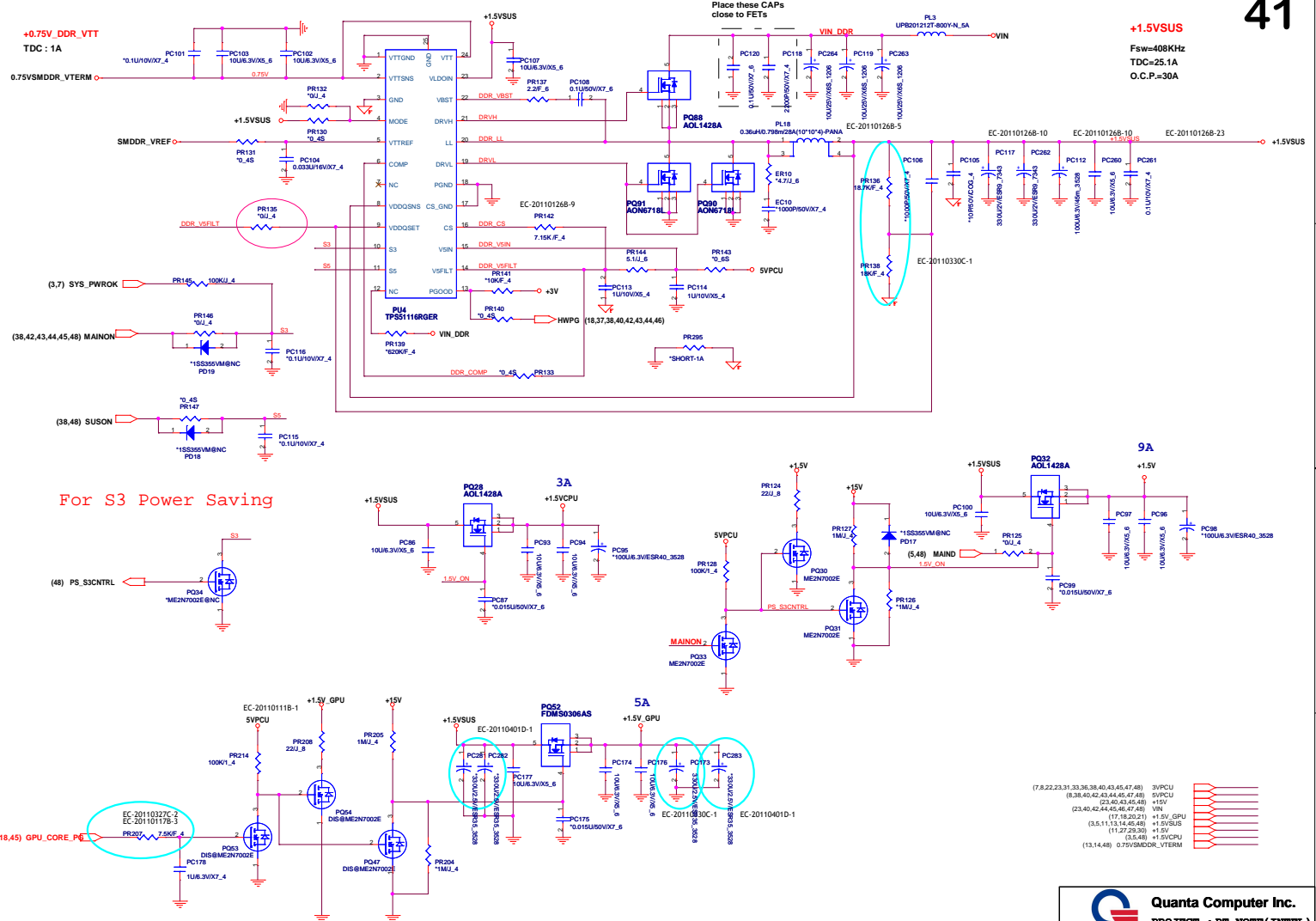


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RFID EEPROM		
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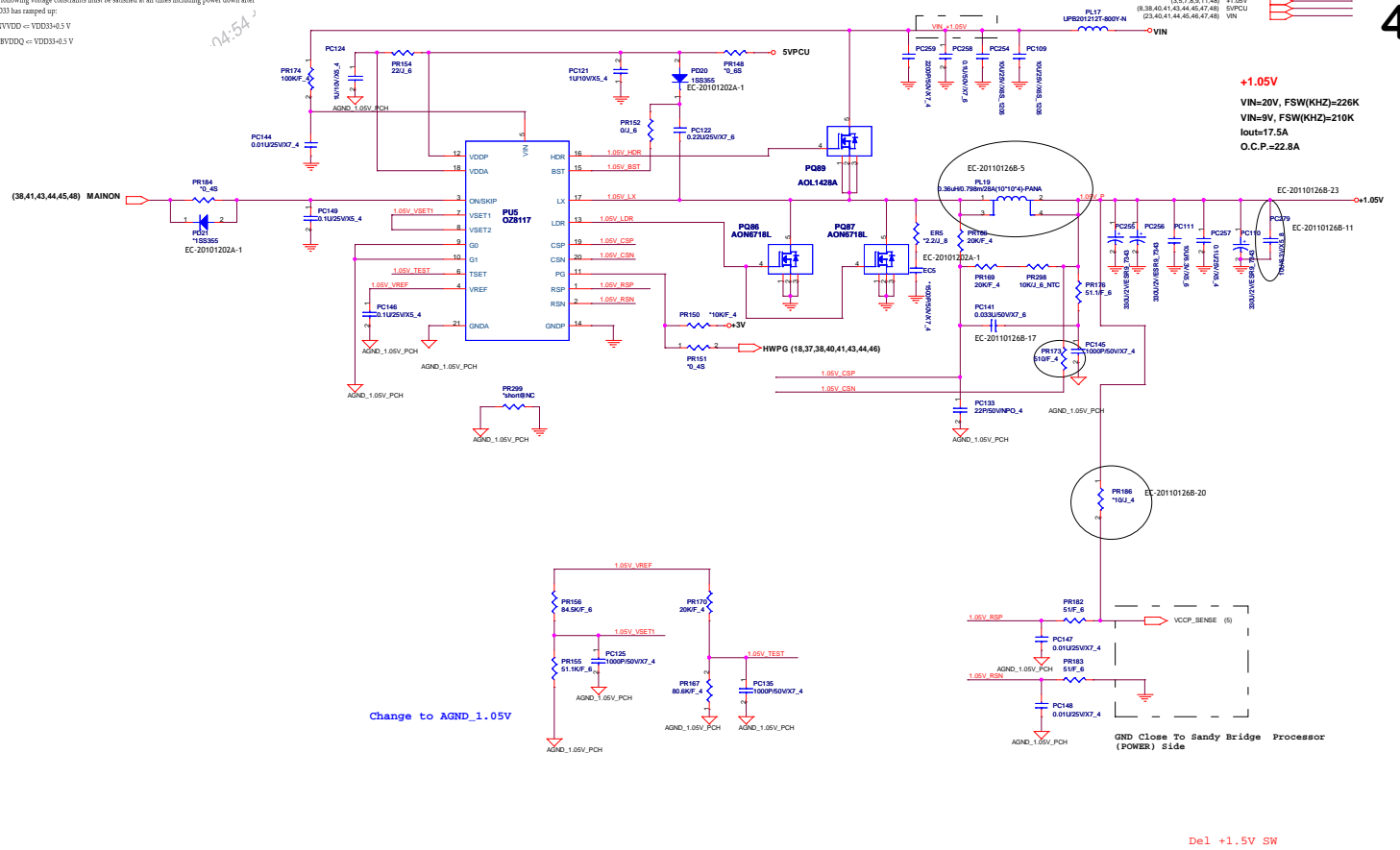


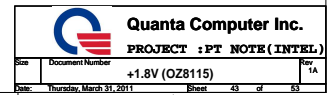


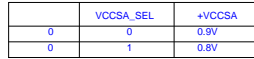


The following voltage constraints must be satisfied at all times including power down after VDD33 has ramped up:

- ▶ $V_{NVDD} < VDD33 + 0.5 \text{ V}$
- ▶ $FVDDQ < VDD33 + 0.5 \text{ V}$

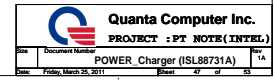


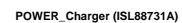


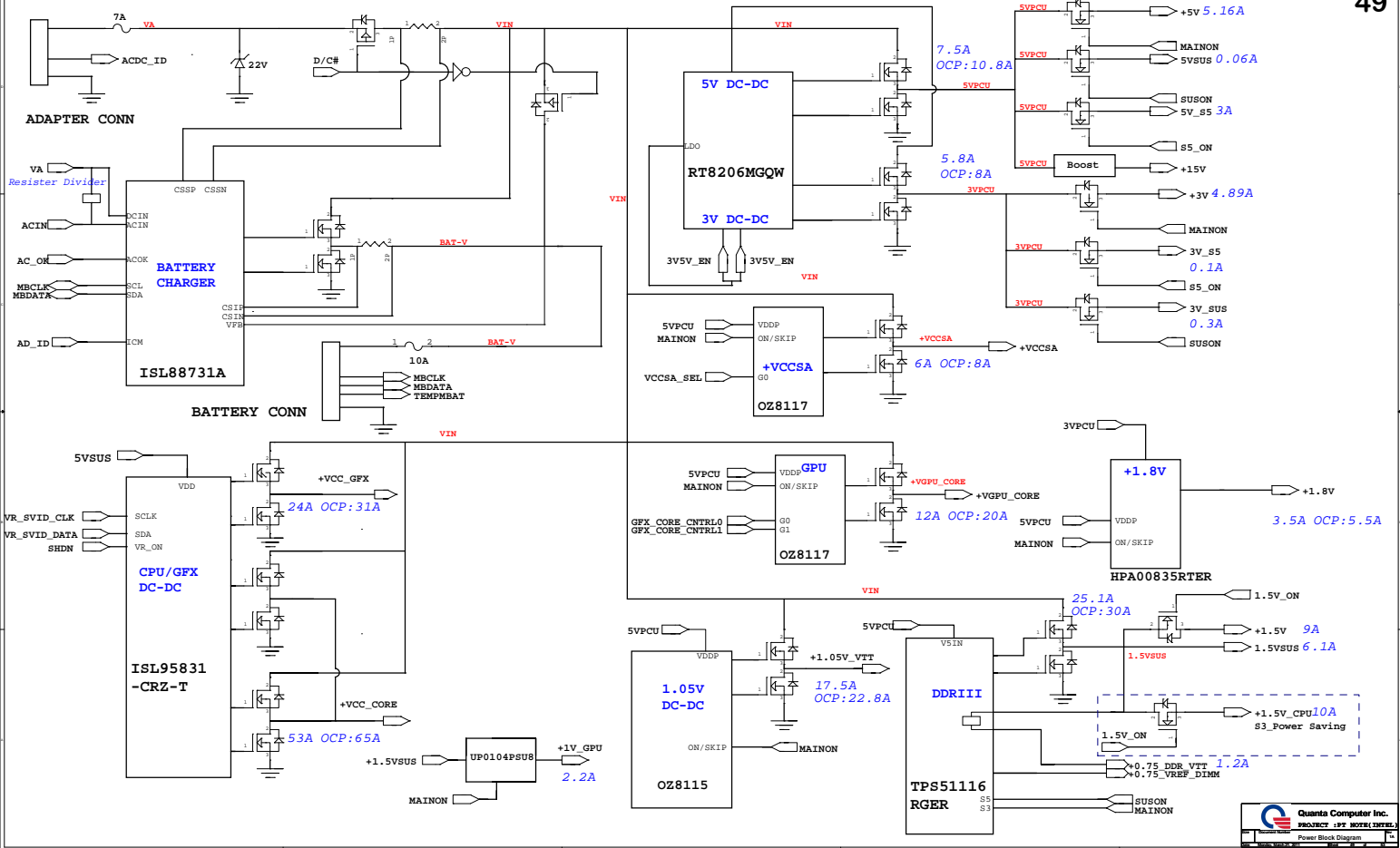












Revision History

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Revision	Date	Phase	Change List	Release Schematic Date	Release Gerber File Date
1A		DV	Initial release		


Schematic Value Explanation Description :

RESISTOR


Value	F	4	6	8	12	1210	*	Description
*1K/F_4	1%	0402 (1005)					DE POP	1K ohm 1% SMD 0402 package and DE POP
1K_6	5%		0603 (1608)				POP	1K ohm 5% SMD 0603 package and POP
1K_8	5%			0805 (2125)			POP	1K ohm 5% SMD 0805 package and POP
1K_12	5%				1206 (3216)		POP	1K ohm 5% SMD 1206 package and POP
1K_1210	5%					1210 (3225)	POP	1K ohm 5% SMD 1210 package and POP

CAPACITOR

Value	Voltage	Material	6				*	Description
*0.1U/10V/X5R_4	10V	X5R	0402 (1005)				DE POP	0.1UF 10V X5R SMD 0402 package DE POP
1U/25V/X7R_6	25V	X7R	0603 (1608)				POP	0.1UF 25V X7R SMD 0603 package POP

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		1A
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EC-20101202A-1	P42	Power RECOMMAND	Power update BOM: PD20,PD21 from BC15S35S205 change to BC15S35S207 ERS and ECS unmount PD10 unmount
	P47		
EC-20101202A-2	P39		BOM error: Update Hole20,Hole21,Hole22,Hole25,Hole26 from MBGC1001010 to FBCW4003010 Hole18 and Hole19 update from MBM3001010 to MBKL7001010
EC-20101206A-1	P38	FAE SUGGESTION	Use EC Internal CLOCK:Y1 and C172 unmount,C169 chngie to 0 ohm 0402



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Rev 1A

Doc

Document Number

Pre A to A-test

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EC-20110111B-1	P41	Net name error:update +SVPCU to 5VPCU
	P45	
EC-20110111B-2	P8 P9 P31	BT Boot issue:BT_DET# change from GPIO55 to GPIO21 of PCH side;GPIO55 of PCH side rename to PCH_GPIO55
EC-20110111B-3	P10 P23	The signal is high active :update CCD power on signal from CCD_ON# to CCD_ON
EC-20110111B-4	P33	ESD RECOMMAND Change C302 package from 0402 to 0603
EC-20110111B-5	P47	Battery connector footprint error :update P.J1P footprint to bat-c144gn-10707-1-7p-1 and swap the signal of P.JP1
EC-20110111B-6	P27	ESATA function fail:update SATA_EN schematic and add SATA_EN_R connection to pin11 of CN18
EC-20110111B-7	P7 P8 P9 P10 P11 P12	Change PCH footprint.It is according to Lenovo request to enhance BGA rework performance, lower the F/R of solder mask or pad peat off from the reworking
EC-20110111B-8	P9 P10 P45	The signal is high active :update VGA power on signal from DGPU_PWR_EN# to DGPU_PWR_EN
EC-20110114B-1	P46	To solve random shutdown issue:Mount 330uF 7343 on PC231
EC-20110114B-2	P23	The netname of U1,L1 swap for easy routing
EC-20110114B-3	P34	Card reader socket error:update BOM and footprint of CN8
EC-20110114B-4	P28	update CN13 Value
EC-20110117B-1	P16 P43	Reserve for fine tune VRAM timing:Add 0 ohm RES R673-R684 on CRT signal
EC-20110117B-2	P45	VGPU_CORE leakage:mount 4.7k 0402 RES on location PR301
EC-20110117B-3	P41 P45 P43	Fine tune VGA power sequence:Mount 30k RES on location PR207, mount 0 ohm RES on location PR304 mount 4.99k res on location PR308, mount 0.1uF cap on location PC272
EC-20110117B-4	P36 P38	The signal is high active :update from GSENSOR_ON# to GSENSOR_ON
EC-20110117B-5	P32	Combo phone jack error:update CN19 pin define and BOM
EC-2010126B-1	P48	Add VGA core schematic:Add PR318,PR319,PQ06,PQ09
EC-2010126B-2	P40	Power RECOMMAND change BOM for EOL material:change from DC-2380M004 to CV-2380M215 of location PL8,PL12
EC-2010126B-3	P43 P44	change BOM for EOL material:change from DC-10B0M007 to CV-10B0M213 of location PL15,PL20
EC-2010126B-4	P47	change BOM for EOL material:change from DC-6845M000 to CV-6845M205 of location PL6
EC-2010126B-5	P41 P42 P45	change BOM for EOL material and update footprint:BOM change from to CV-36T0M201 of location PL18,PL19 and PL21
EC-2010126B-6	P46	change BOM for EOL material:BOM change from to CV-36T0M201 of location PL9,PL10 and PL11
EC-2010126B-7	P46 P45 P41	change BOM for EOL material:BOM change from to CV-36T0M201 of location PQ11,PQ12,,PQ16,PQ17,PQ45,PQ46 PQ43,PQ47,PQ49,PQ51
EC-2010126B-8	P40	Reduce power noise:Add PC278 and PC35 unmont
EC-2010126B-9	P41	Fine tune +1.5V5US OCP:chnage BOM to CS27152F819 of PR142
EC-2010126B-10	P41	Reduce power noise:change BOM to CH733R18802 of location PC117 and PC162, and mount 10uF on location PC260
EC-2010126B-11	P42	Reduce power noise:Add PC279
EC-2010126B-12	P44	For stable VCCSA switching:change BOM to CS-2203F911 on location: PR281
EC-2010126B-13	P45	For stable GPU switching:change BOM to CS-2203F911 on location: PR191
EC-2010126B-14	P45	Reduce power noise:Add PC280
EC-2010126B-15	P45	Fine tune VGPU_CORE OCP:chnage BOM to CS14532F814 of PR162
EC-2010126B-16	P40	Fine tune 3VPCU OCP:chnage BOM to CH41472F816 of PR95
EC-2010126B-17	P42	Fine tune 1.05V OCP:chnage BOM to CS15102F819 of PR173
EC-2010126B-18	P46	Reduce GFX power noise:1.PC182 mount 2.Change BOM to CS10202F829 of location PR234 3.Change BOM to CS14532F814 of location PR28 4.Change BOM to CS22102F814 of location PR229 5.Change BOM to CS14922F816 of location PR40 6.Change BOM to CS31822F816 of location PR223
EC-2010126B-19	P46	For stable Vcore voltage:change BOM to CS21072F818 on location: PR244
EC-2010126B-20	P42	For 1.05V delete jump:PR186 unmont
EC-2010126B-21	P44	For VCCSA delete jump:PR282 unmont
EC-2010126B-22	P45	For VCCSA delete jump:PR158 unmont
EC-2010126B-23	P41 P42 P43 P44 P45	Delet P.JP2-P.JP15
EC-2010126B-24	P23	RF RECOMMAND Reduce RF noise:1. 2200pF on location C747 and C753 2. 5.4pF on location C541 and C542
EC-2010126B-25	P38	Reduce S5 leakage:R117,R81,R76 unmount
EC-2010126B-26	P32	ME RECOMMAND To differentiate between SPK connector and INT MIC connector:change BOM to DF1504FRC51 of location C06
EC-2010126B-27	P8	To follow DR4 RTC battery:Change BOM to DF1602M5119 and update footprint to 50271-00201-001-2p-1 of BT1
EC-2010126B-28	P3	Reduce sigal noise of PECl bus:Reserve 5.6pF on location C803
EC-2010127B-1	P46	Power RECOMMAND For stable GFX voltage responses:Change BOM to CS00002JB38 of location PR246
EC-2010127B-2	P35	Thermal RECOMMAND Due to add FAN_ID:update BOM and footprint of location CH6,FAN_ID connect to pin79 of U10
EC-2010127B-3	P40	Reduce signal noise of SYS_SHDN#:reserve 0.1uF cap on location C804
EC-2010216B-1	P26	FAE RECOMMAND Fine tune ESATA signal:mount 4.7k Res on location R533 and R534

EC-20110321C-1	P39	ME RECOMMAND	For ME Base design change:Delete Hole8 and add Hole27, Hole28,Hole29,Hole30
EC-20110321C-2	P39	ESD RECOMMAND	GND pad for ESD solution:Add PADS and PAD6
EC-20110321C-3	P35	EC RECOMMAND	Add pull up RES R677 for FAN ID
EC-20110321C-4			Update 0 ohm to short pad
EC-20110321C-5	P32		To fine tune Audio code pcbbeeper gain control:R699 mount 10k ohm 0402
EC-20110321C-6	P13	EMI RECOMMAND	Reduce +1.5VSUS noise:Add C810 3.3P/NPO 0402 and closed to ram socket
	P14		Reduce +1.5VSUS noise:Add C811 3.3P/NPO 0402 and closed to ram socket
	P34		update R383,R358,R350,R343,R338,R326 from 0 ohm to 22 ohm 0402
			mount 10PF 0402 on location C480
	P38		Add 33PF 0402 cap on location C807 for reduce B512_CLK1 noise and closed to EC flash part
	P30		Reduce +1.5V and +3.3V_WWAN noise:mount C545 and C548 of 33P/NPO 0402
	P35		Add C805 and C806 cap 220pf for EMI solution
	P47		Reserve C808 and C809 10pf 0402 for battery smbus
EC-20110321C-7	P22		Delete CiliPad unused power of +3V and remove R111
EC-20110323C-1	P23		To fine tune logo LED brightness:1.update from 330 to 1k ohm on location R400
	P36		2.update from 510 to 1k ohm on location R464
EC-20110323C-2	P33		To fine tune RJ45 LED brightness:1.update from 150 to 680 ohm on location R145
			2.update from 150 to 510 ohm on location R171
EC-20110323C-3	P16		To fine tune Y4(27M crystal) :update from 18pf to 10pf ohm on location C751 and C745
EC-20110323C-4	P30	Lenovo RECOMMAND	Updathe MSATA speed to gen2:Add MSATA redriver IC schematic
EC-20110327C-1	P18		Reduce +VGPU_CORE GPU side power noise
			Add capacitance: mount 4.7uf 6.3V cap on loaction C272,C338,C330,C341,C300,C340,C301,C274,C339,C265
			mount 47uf 6.3V cap on loaction C740,C741,C744
EC-20110327C-2	P41		Fine tune VGA power sequence:mount 7.5k resistance on loaction P8207
	P45		mount 60.4k resistance on loaction P8308
			mount 51k resistance on loaction P8201
			mount 0.1uf capacitance on loaction PCT72
EC-20110330C-1	P20		Improve +1.5V_GPU heavy loddading voltage drop:
	P21		1.Update +1.5VSUS output voltage from 1.5V to 1.53V - PR136 mount 18.7k resistance and PR138 mount 18k resistance
	P41		2.Add capacitance: Add 1uf 6.3V cap on loaction C661,C195,C722,C381,C418,C180,C621,C164
			Add 0.1uf 10V cap on loaction C703,C319,C720,C471,C640,C145,C630,C147
			Mount 330uf ES835 on loaction C136 and P6172
			3.Change low RD5_DN WDS:change BAMB3060001 to PC62
EC-20110330C-2	P36		To fine tune logo LED brightness:update from 510 to 0 ohm on location R407
EC-20110331C-1	P32	ESD RECOMMAND	Update R600 and R618 from 75 ohm to 39 ohm
			Update C743 and C766 BOM to BC0051P200
EC-20110331C-2	P28		EA fail:Del CML3 , mount R479 and R480
EC-20110401D-1	P18		Reduce power noise and voltage drop:1.+VGPU_CORE:Add 330uf 3538 Cap on location C822
	P42		2..+1.5V_GPU:Add 330uf 3538 Cap on location PC281,PC282 and PC283